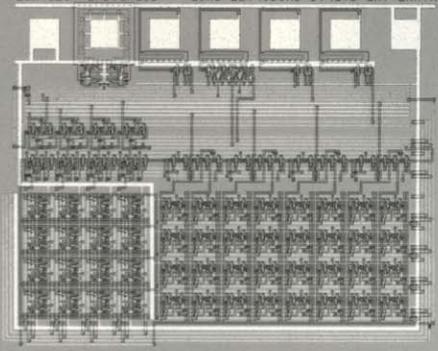
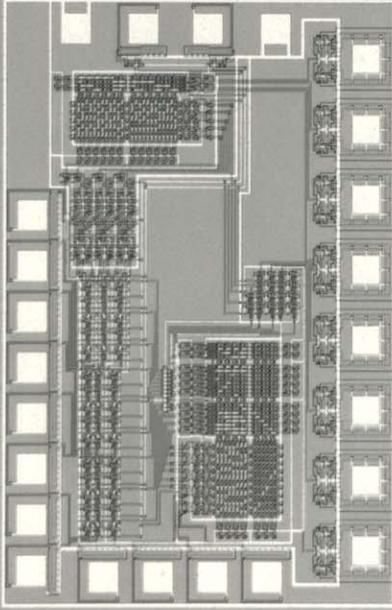


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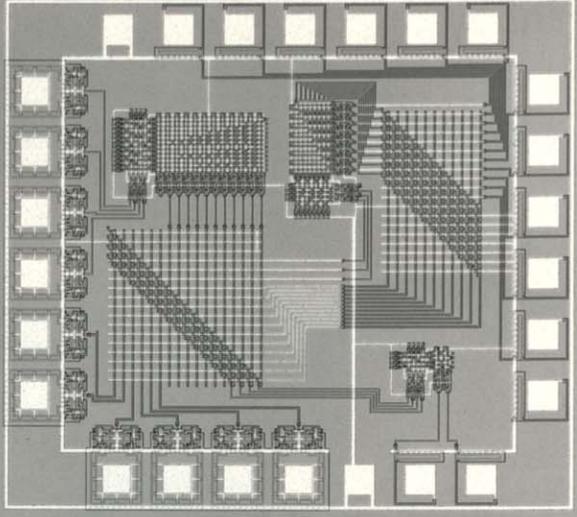
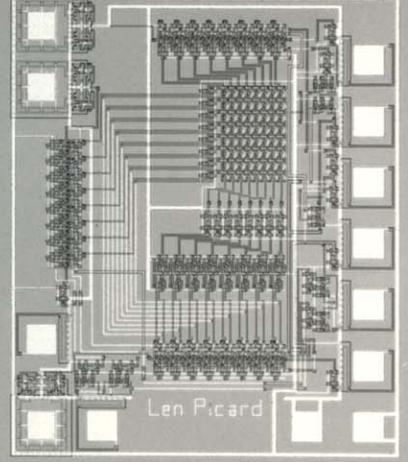
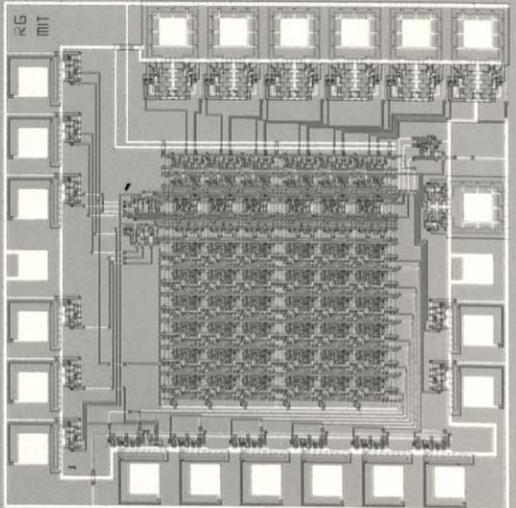
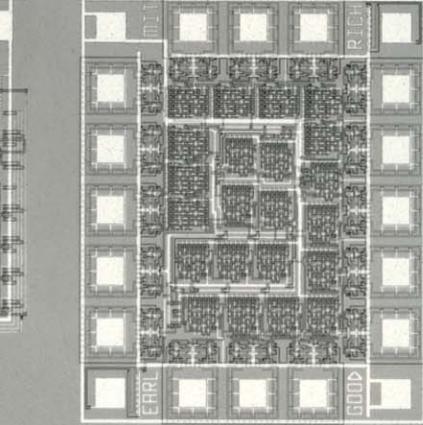
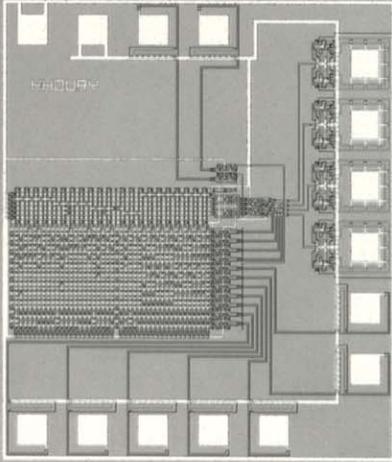
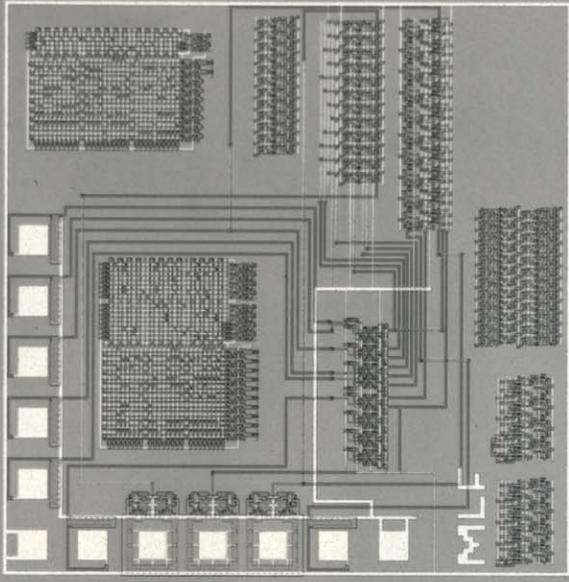


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 POL +
   
 CAT +
   
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**MPC79AB**  
 1972 Microprocessor - Source Chip  
 Neo-Thom-Store, Inc. © SSL  
 Data Conversion, Inc. © SET  
 Home Electronics, Inc. © HET  
 Curtis Inc. © Curtis, Inc. © C-I



WAYNE AND CARL'S CHOCOLATE CHIP -- COPYRIGHT 1979 MIT







MPC79AE

1979 PLASMAKIT PROJECT CAP  
New Micro-Mark, MRC/SSL  
Disk Emu, WIPANET  
Laser File Relay (P/Printer/ACU)



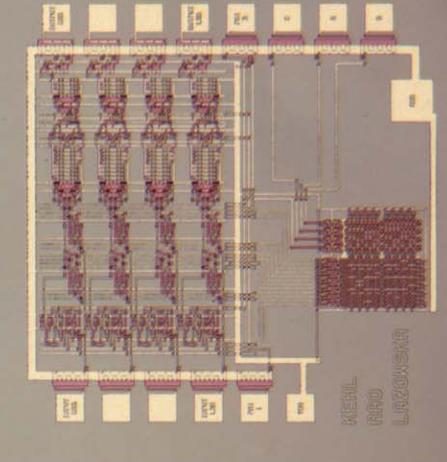
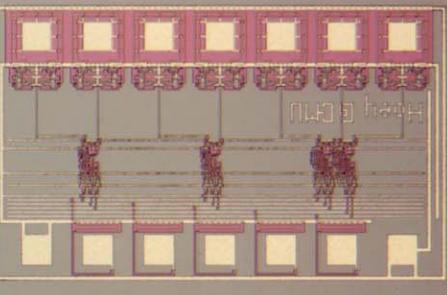
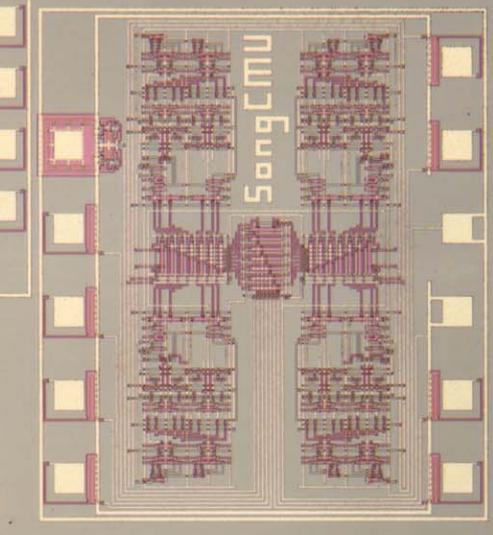
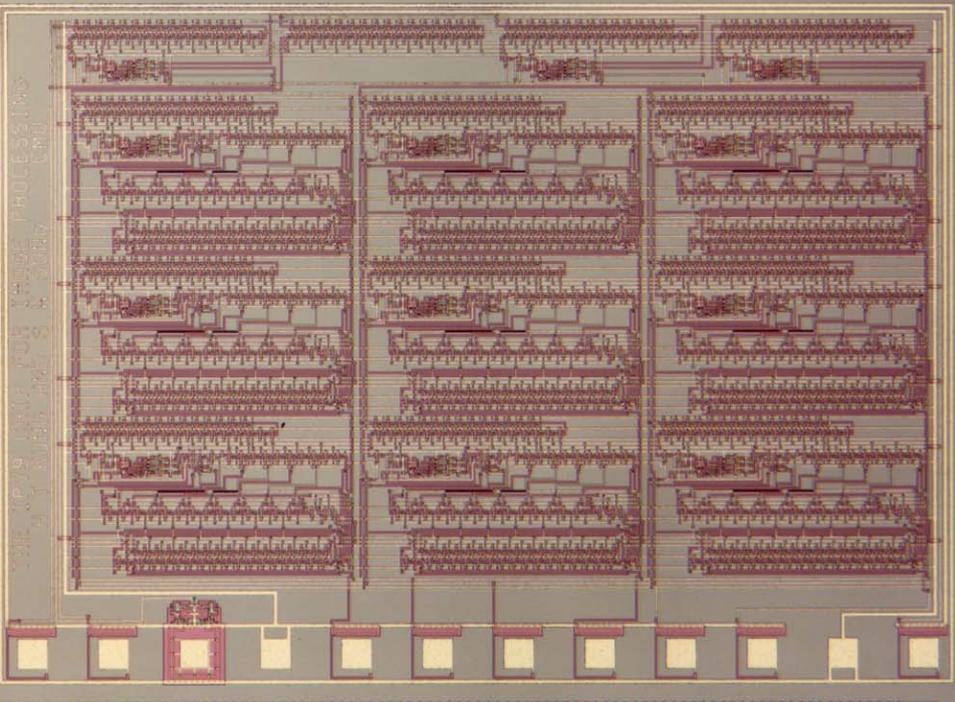
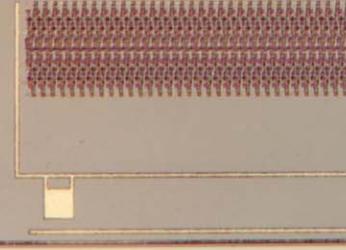
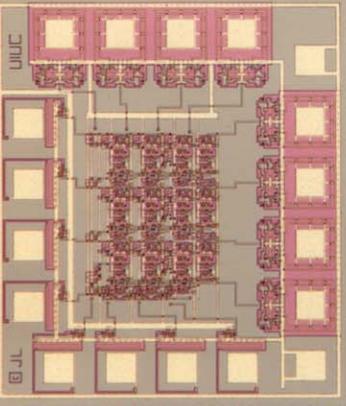
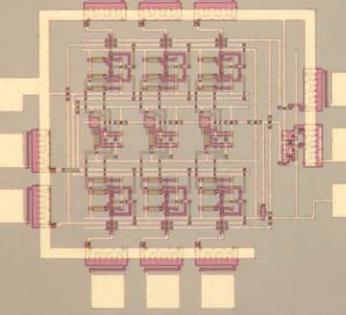
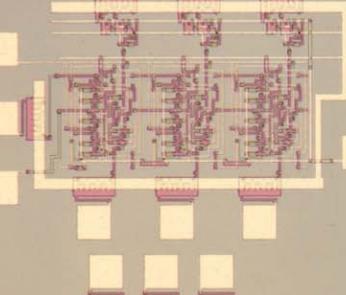
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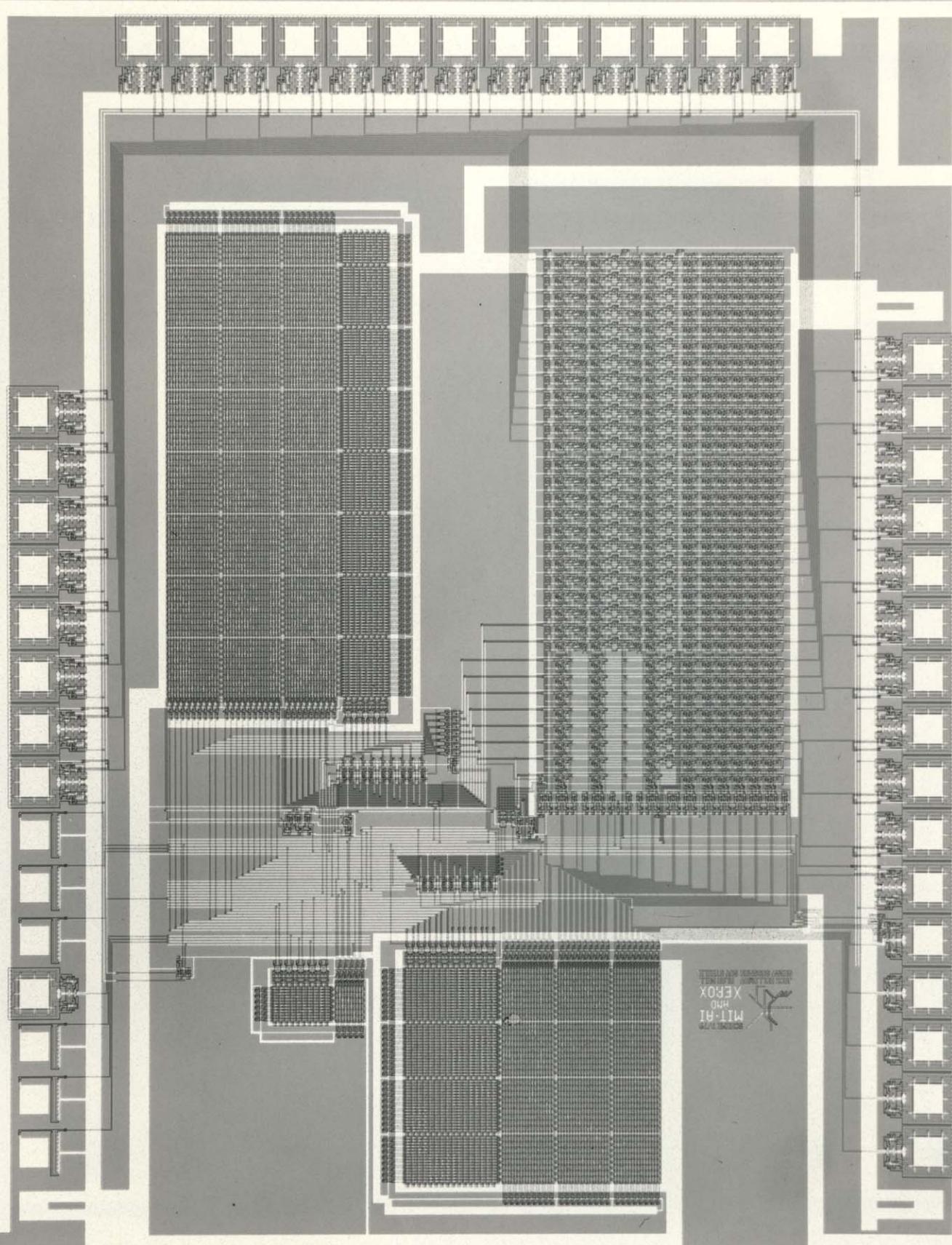
HARRY CPU

REIL CPU  
LAWSON

MPC79AF

3.3V 1.8V 1.5V 1.2V 1.0V 0.8V 0.6V 0.5V 0.4V 0.3V 0.2V 0.1V

PCCL +  
MET +  
CLUT +  
DUF +



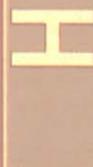
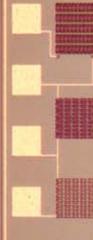
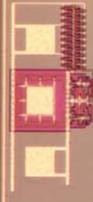
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MPC79AH

DESIGNED BY  
KEROX PARC/SSL  
AT THE UNIVERSITY OF CALIFORNIA  
SAN DIEGO, CALIF. 92161  
FOR THE QUALITY ASSURANCE  
DIVISION OF HEWLETT-PACKARD



## MPC79:

The demonstration of a prototype 14400-bit, fast-turnaround, VLSI Implementation System

During the fall of 1979, the LSI Systems Area of Xerox PARC/SSL concluded the demonstration operation of a prototype information management system, designed by Alan Bell and Martin Newell, for enabling the remote-entry, fast-turnaround implementation of large numbers of VLSI system designs. The user community for this demonstration was composed of EE/CS students taking courses in VLSI design at a number of universities, and university faculty and research staff members undertaking research prototype designs. MPC79 contains 35 designs submitted by ~100 designers from many different universities.

The purposes of this effort were (i) to support the new university VLSI design courses by providing the implementation of student-project designs, (ii) to demonstrate the feasibility and general capabilities of such VLSI implementation systems to a wide technical community, and (iii) to refine our ideas about how to architect, design, and operate such systems by running a major operational test of a prototype system.

Several other organizations collaborated with us in conducting this demonstration: Data Communications (electronic messages and design file transfers) were supported by use of the APPAMET; Maskmaking was done by Micro Mask, Inc., using an electron-beam mask-making system; Wafer fabrication was done by Hewlett-Packard's Integrated Circuit Processing Laboratory.

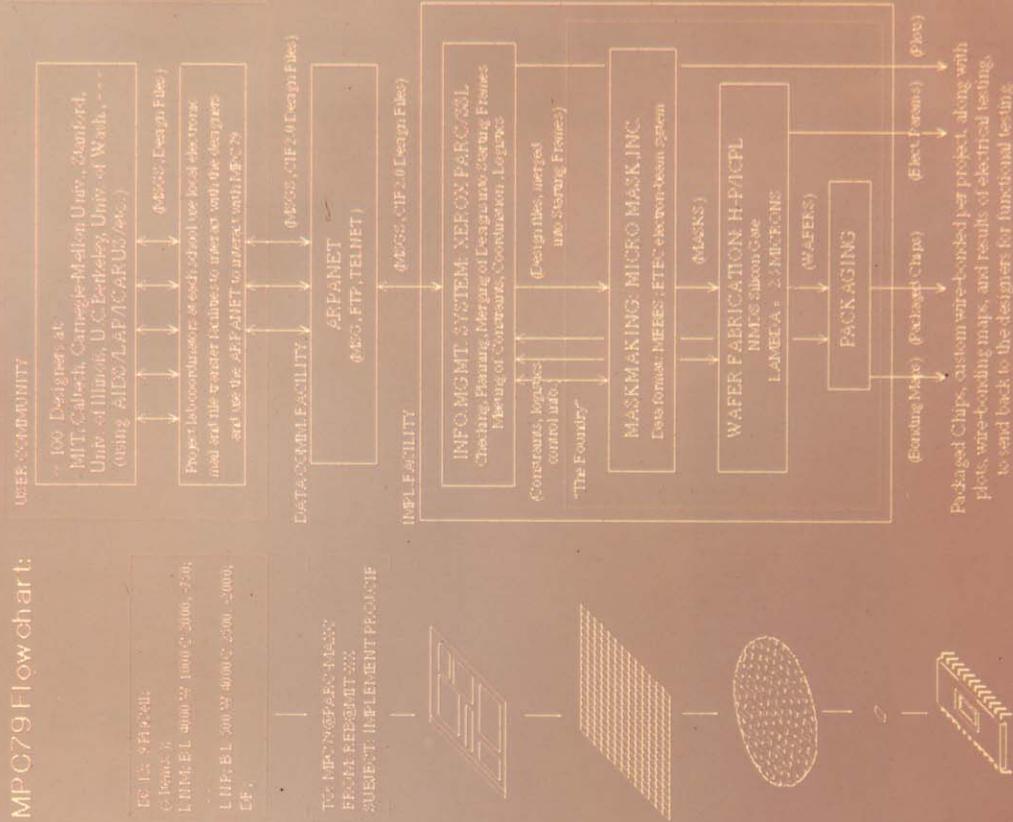
The VLSI design techniques used in the university courses are described in the textbook *Introduction to VLSI Systems*, by C.Mead and L.Conway, Addison-Wesley Publishing Co., 1980.

The background and context of the MPC79 effort, the structure of the MPC79 system, and the final results of the effort will be described in a Xerox PARC/SSL Report by L.Conway, A.Bell, and M.Newell entitled *The Implementation of VLSI Systems*.

## The MPC79 Organizers:

Lynn Conway, Alan Bell, Martin Newell, Richard Lyon  
LSI Systems Area, Xerox PARC/SSL  
4 December 1979

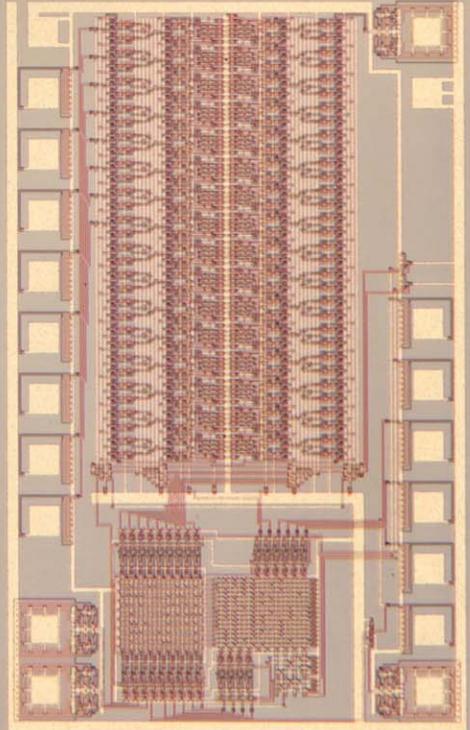
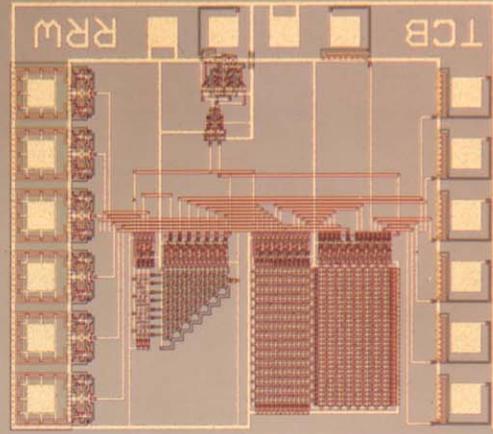
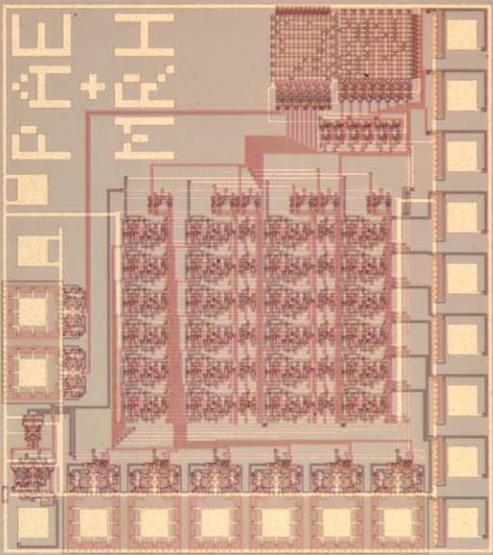
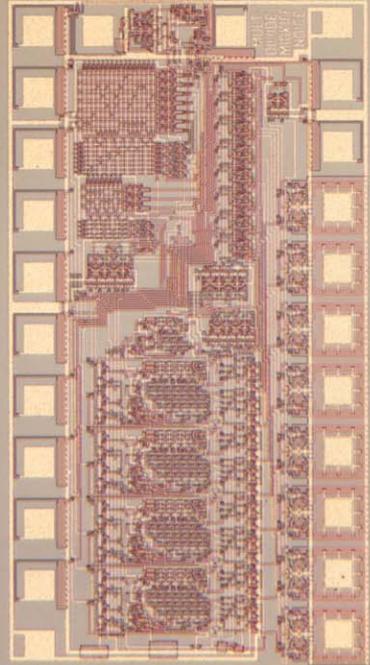
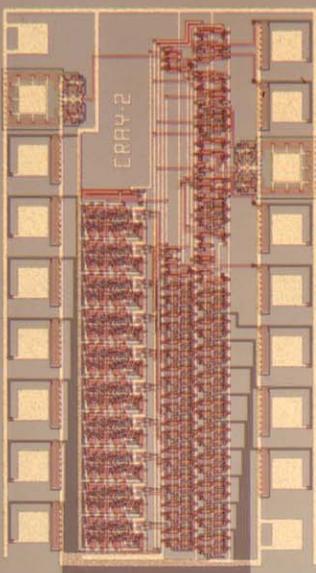
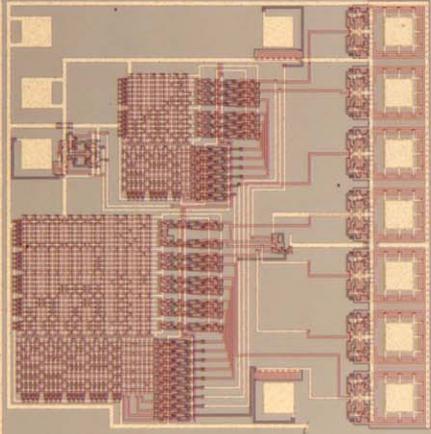
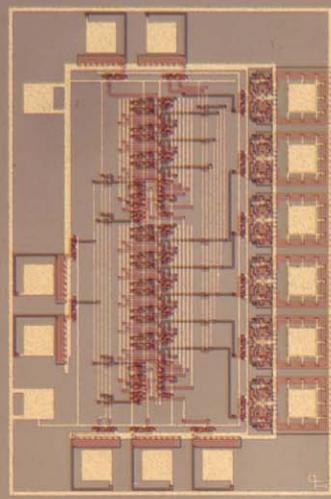
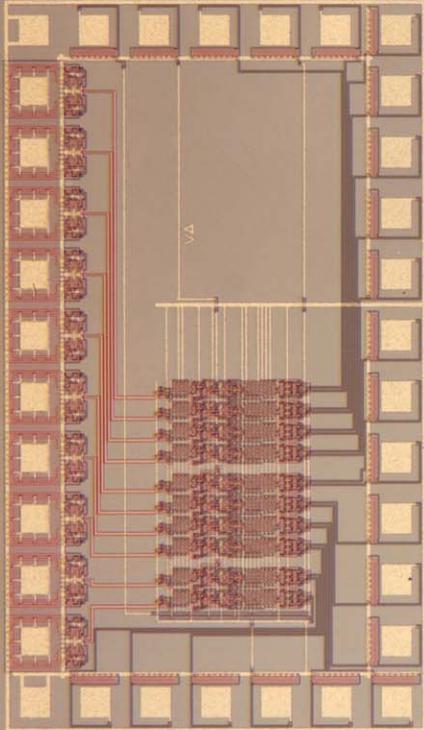
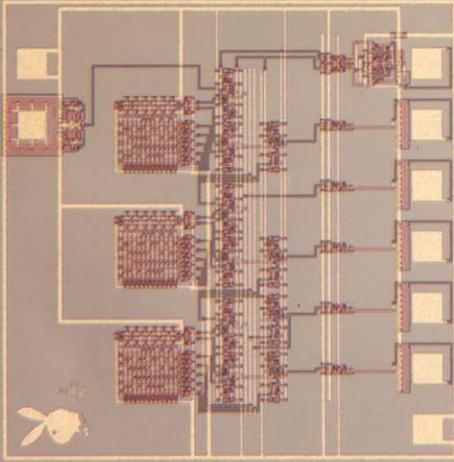
## MPC79 Flow chart:



MPC7981

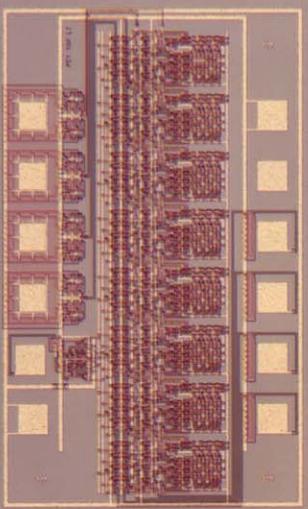
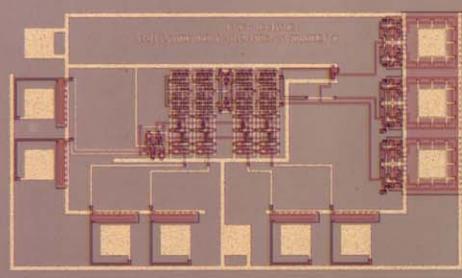
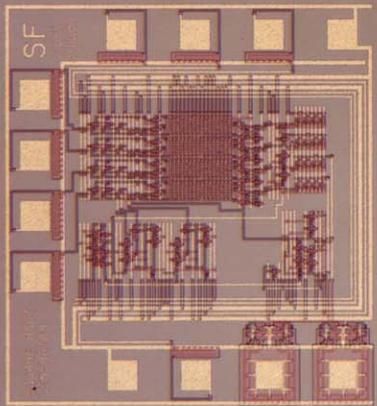
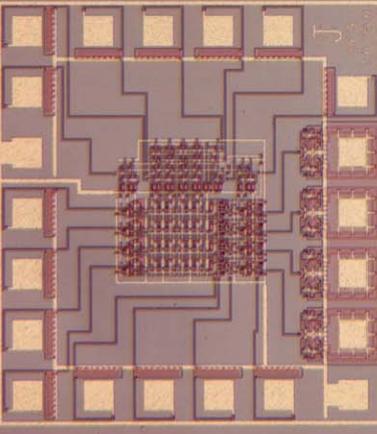
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100% TESTED  
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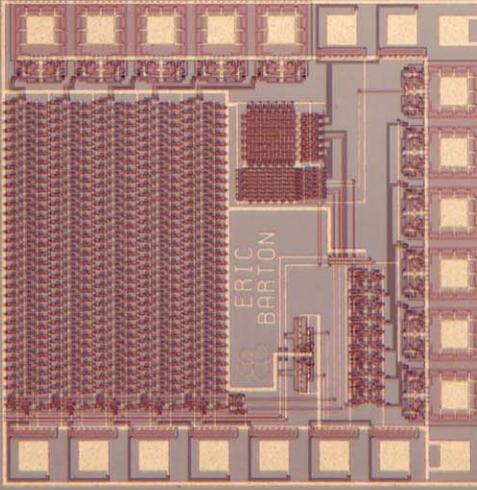
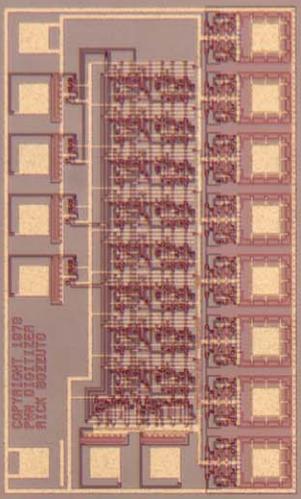


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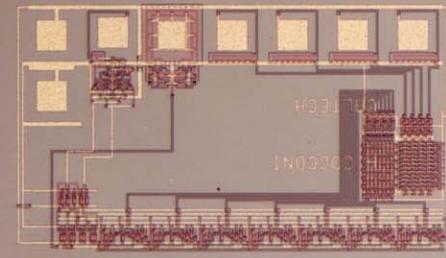
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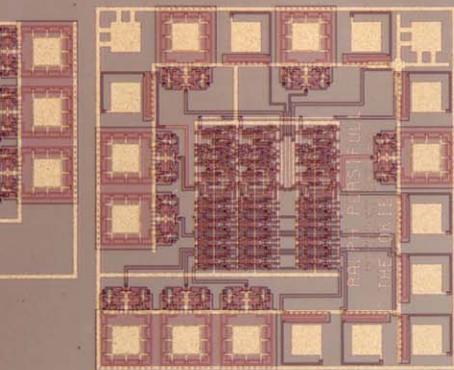
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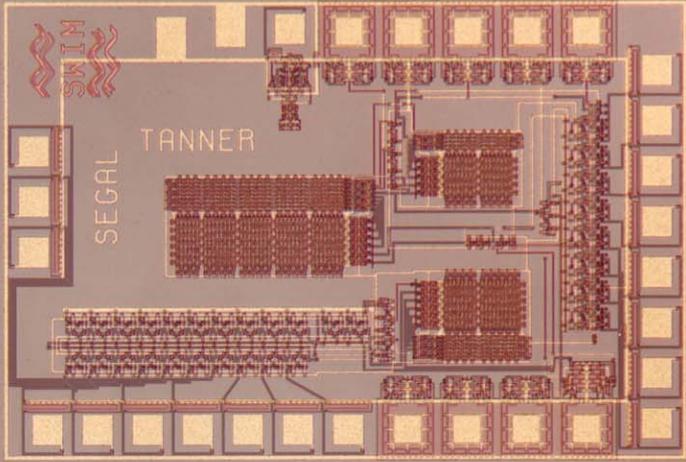
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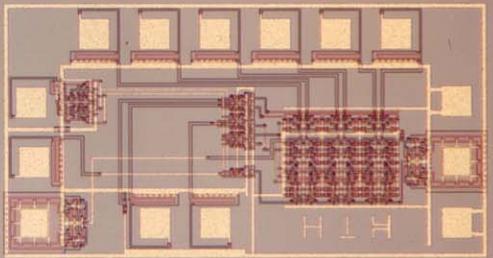
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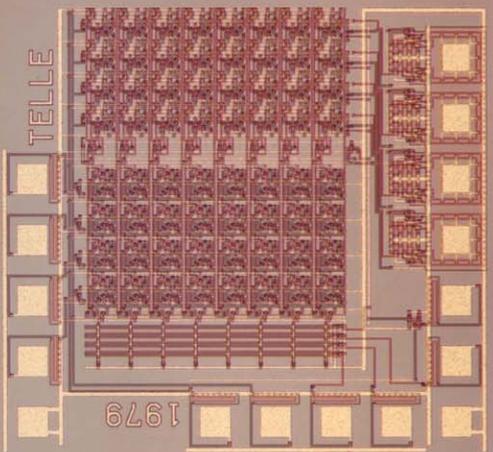
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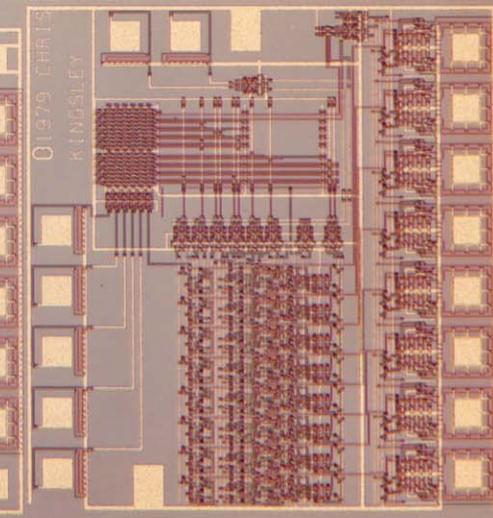


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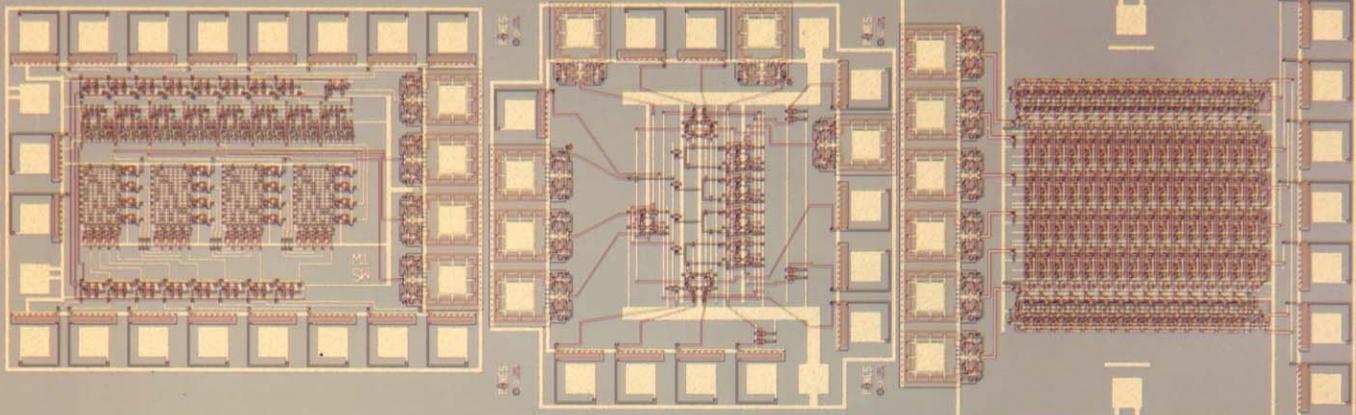
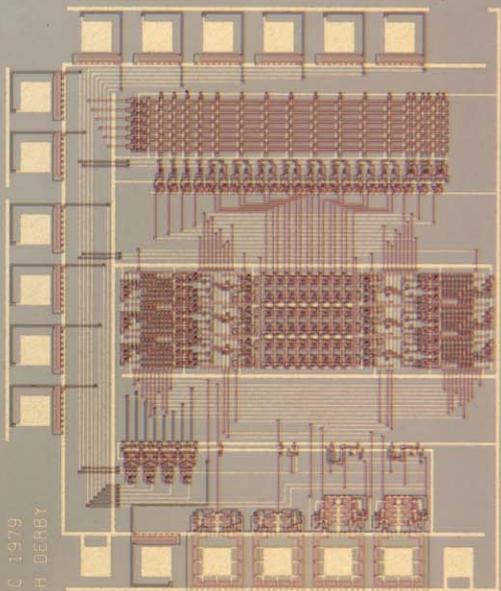
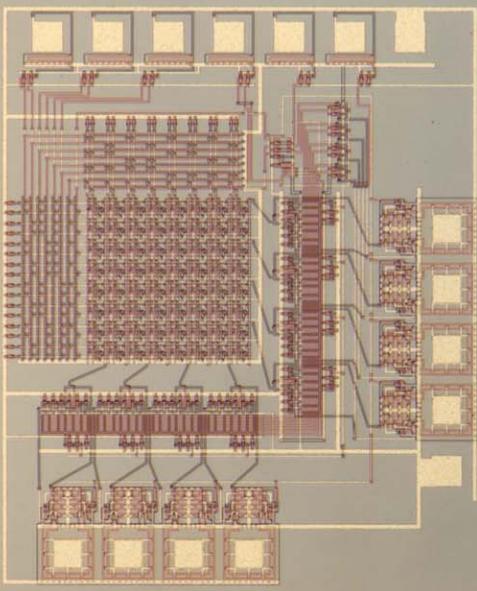
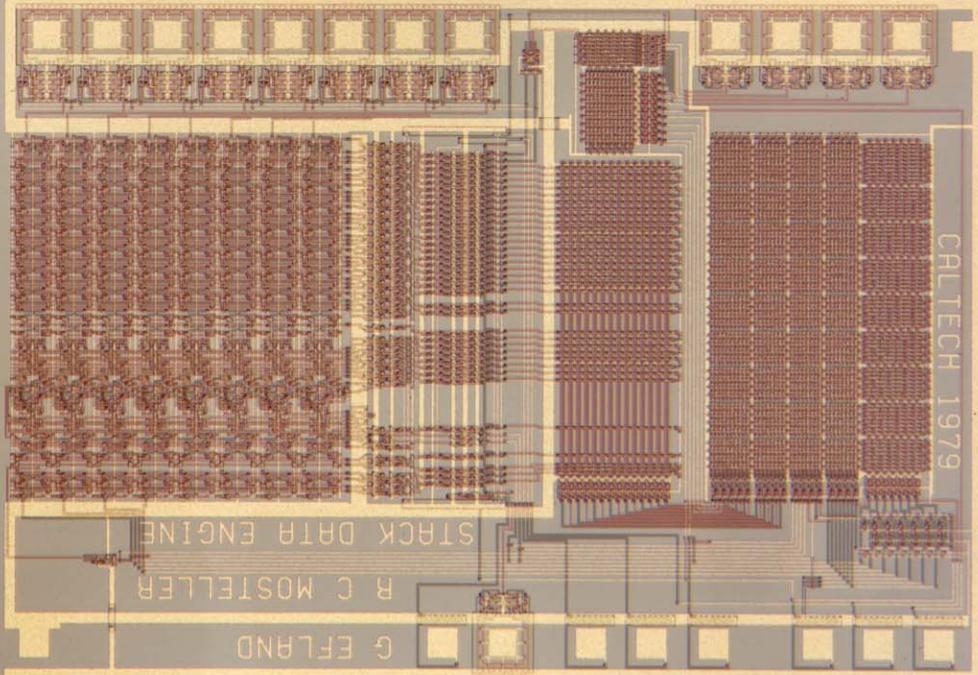
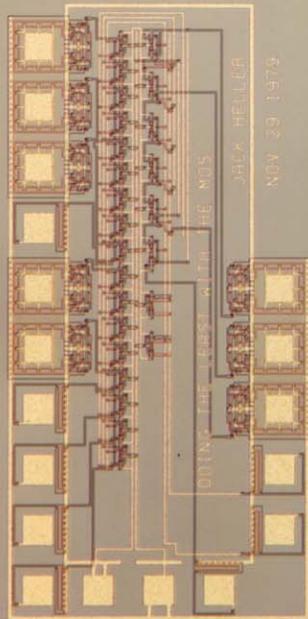


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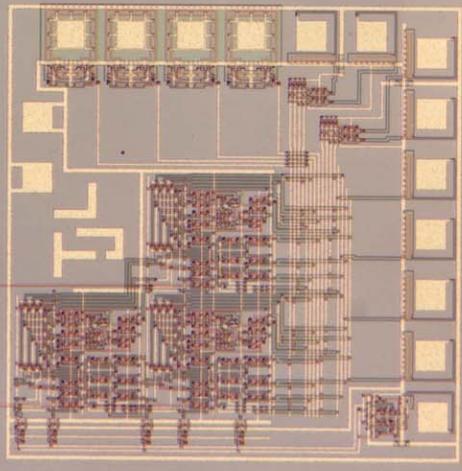
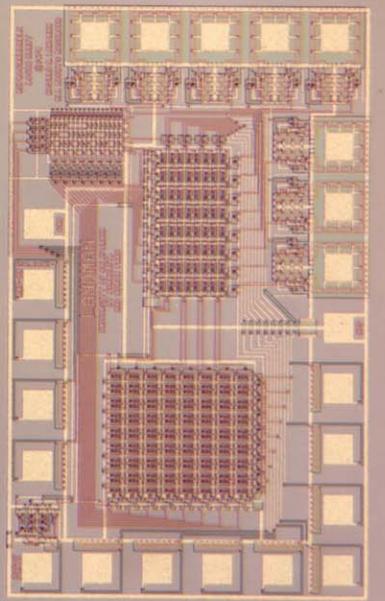
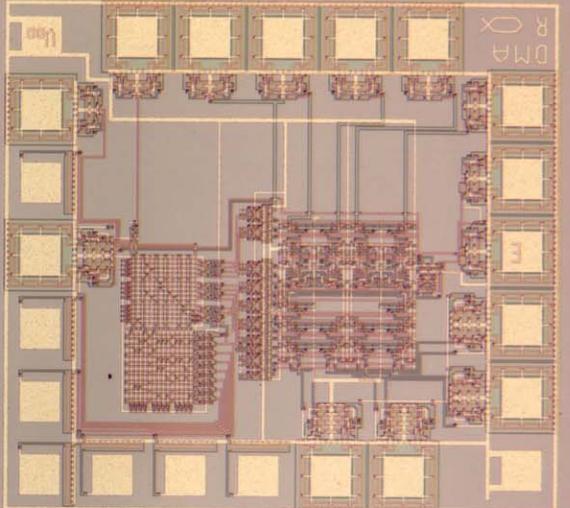
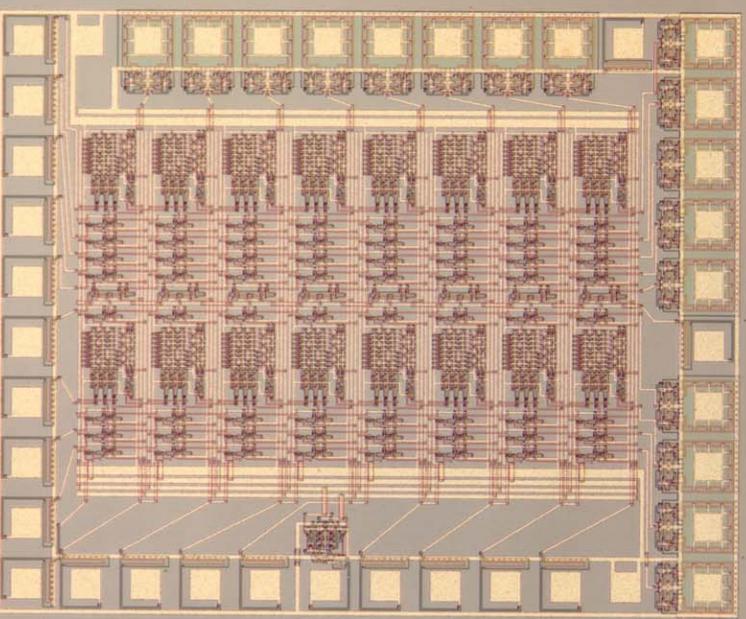
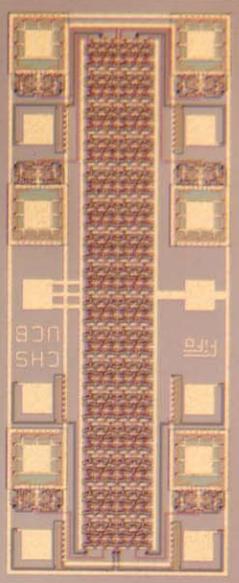
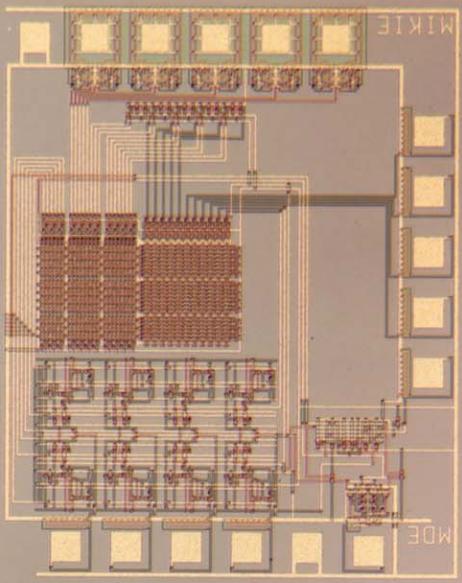
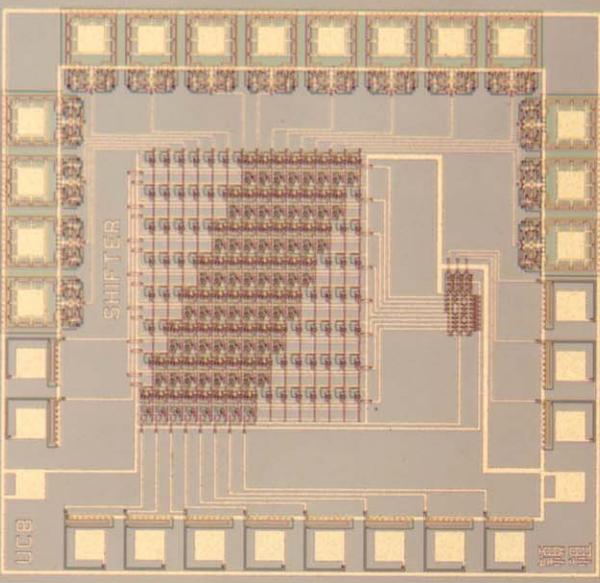


MPC798L

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M



MPC796M  
 100% Lead-Free  
 100% Halogen-Free  
 100% RoHS Compliant  
 100% REACH Compliant  
 100% ISO 9001 Certified

DXP +  
 MET +  
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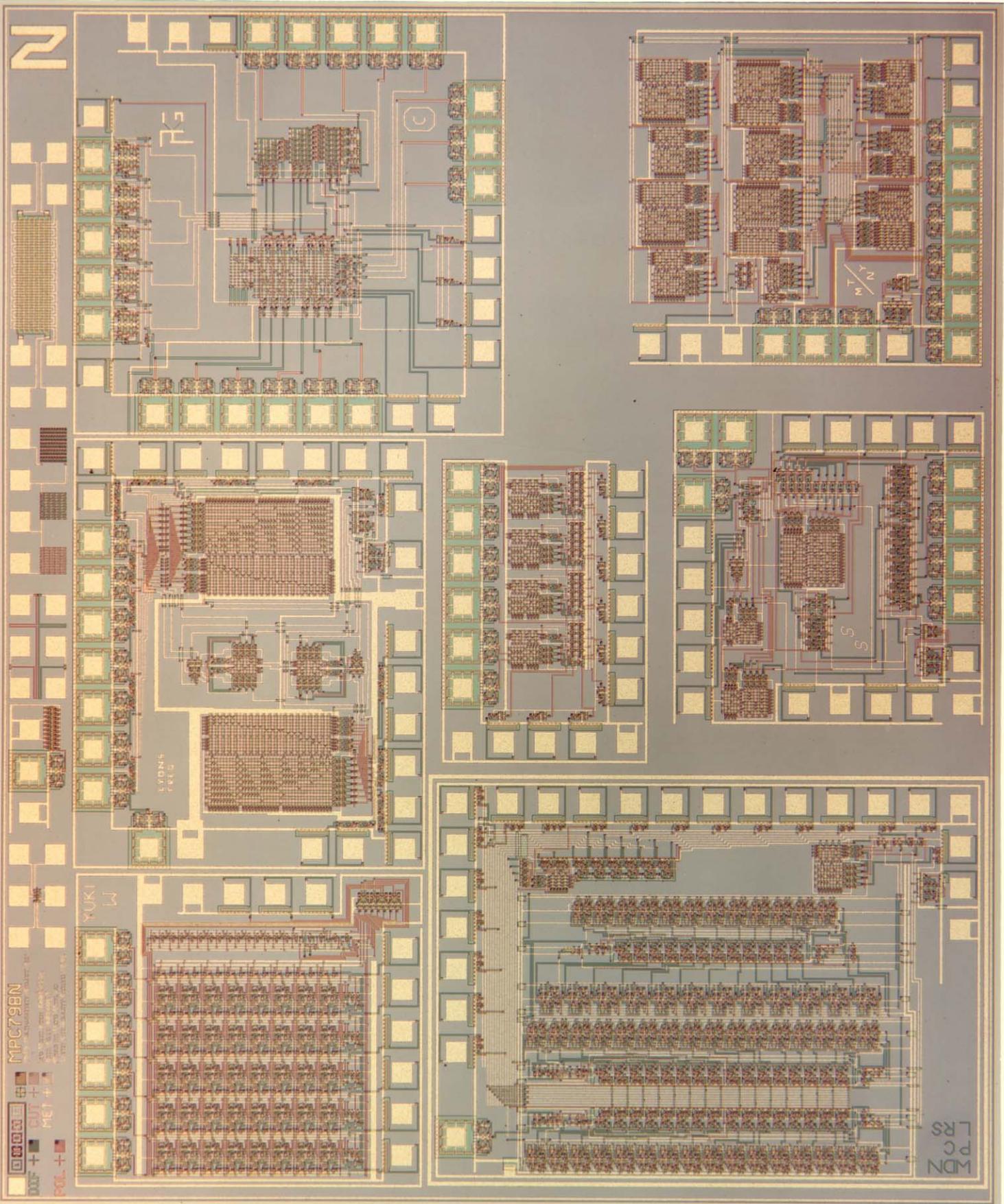
MPC798N  
MPC798N is a 32-bit floating point processor.  
It is designed for use in high performance  
computers and workstations.  
For more information, contact Intel Corp.

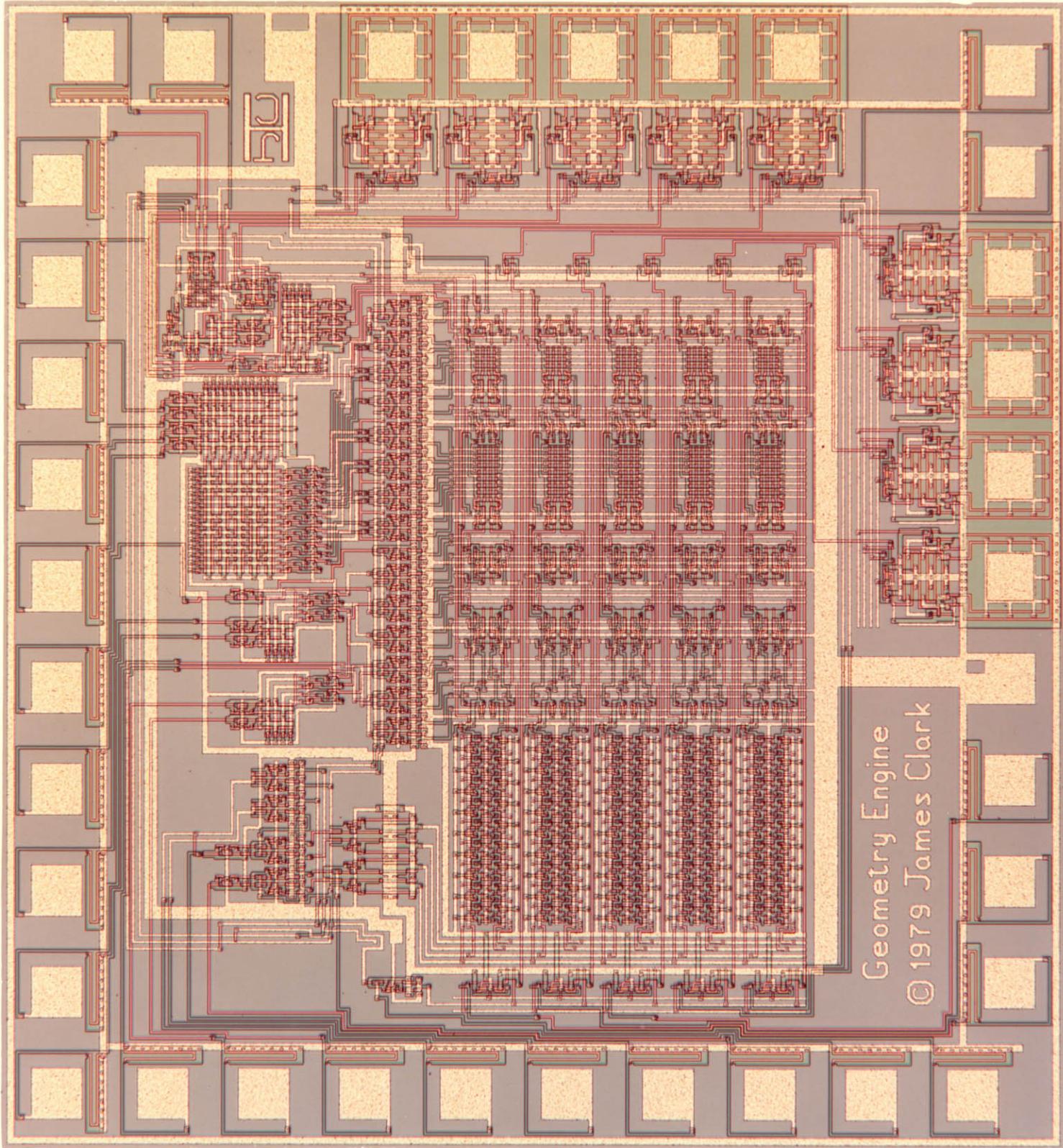
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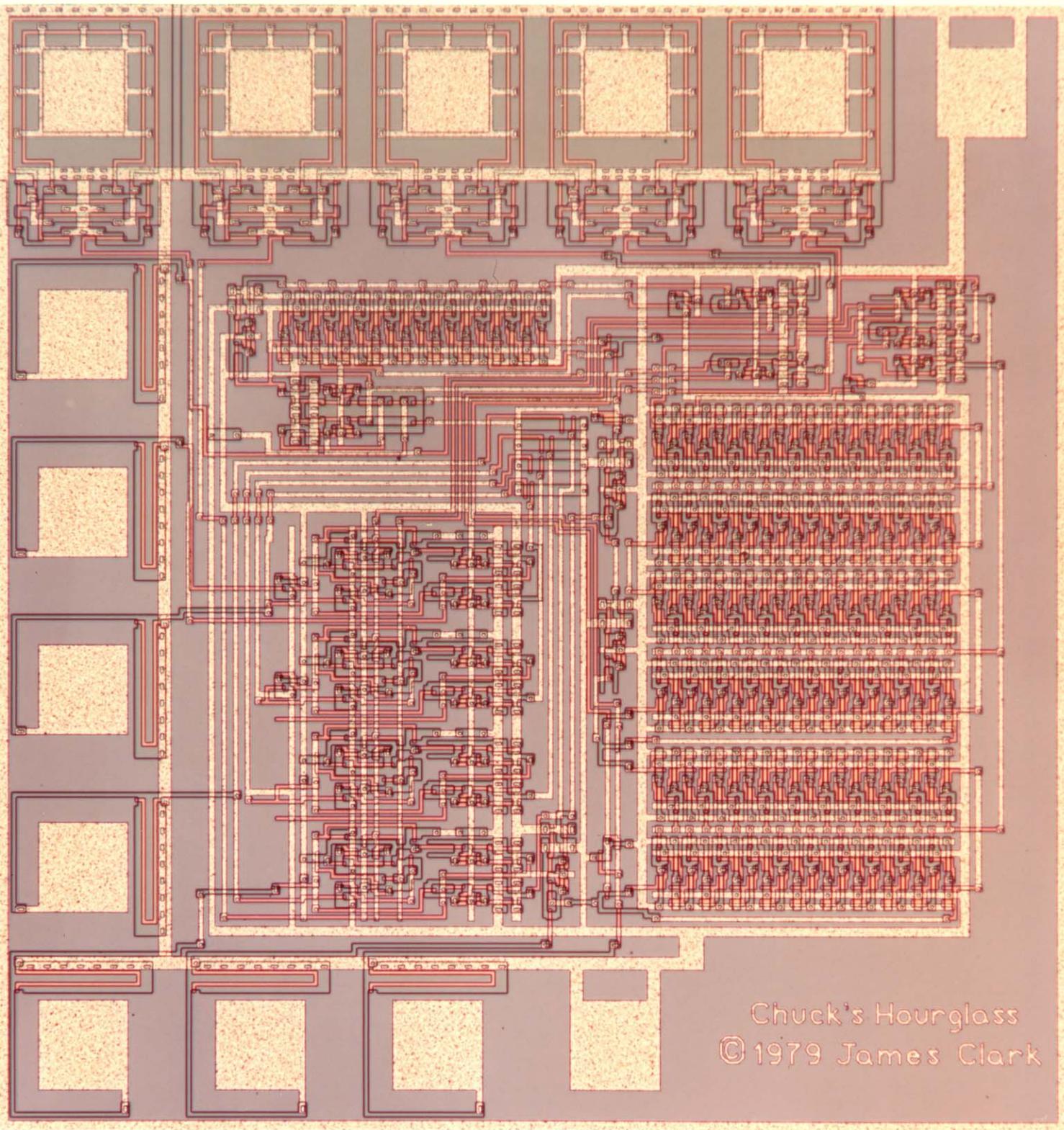
1 XDN5  
CPU 2

PC  
MDN  
LRS





Geometry Engine  
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Chuck's Hourglass  
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