

## SEMINAR

NOVEMBER 28

[NOTE: CONTACT OTTEN, CHERRY, FOR PRES. OF THEIR PROJECTS NEXT TIME. ALSO: SNYDER: DIMENSIONS? FILE TO PARC?]

- TODAY WE HAVE ANOTHER VISITOR, WHO WILL BE GIVING A SEMINAR. BEFORE WE BEGIN, I'D LIKE TO BRING YOU UP TO DATE ON THE PROJECTS; ALSO, NEXT TIME WE WILL BE SPENDING THE ENTIRE LECTURE ON PROJECTS - SOME STUDENTS WILL BE DESCRIBING THEIR PROJECTS - AND I'LL BE COVERING LOTS OF LAST MINUTE DETAILS RELATED TO THE PROJECT CHIP.
- DESCRIBE STATUS LIST (ON BOARD).
- WILL SHIP FILES TO PARC OVER ARPANET TODAY, BET 5 & 6 PM, FOR PLOTTING. THEY'LL BE BACK IN TIME FOR CLASS ON THURSDAY. ALL THE CATEG #1 FILES, & ANY OTHERS IF YOU SEE ME AND LET ME KNOW STATUS / FILE NAME.
- HAVE ROOM FOR CATEG 1, 2, + a couple of more. WILL COMMIT AREA AS I SEE PLOTS, GET FINAL DIMENSIONS.
- HAVING A PLOT COME BACK THURSDAY THAT LOOKS OK WILL BE GOOD WAY TO INSURE YOUR PROJECT GETS ON.
- LIBRARY CELLS: MUST MAKE CHANGE IN PaD VDD:  
SEMICOLON MISSING: (12 ITEMS);  
LNGLS; BOX ---;  
optional: one of the boxes is slightly too small if looks wierd if plotted on large scale.  
optional: Show PaD In:  
You could simply replace PaD VDD, PaD In with the new updated versions in <LSI.CELLS>
- ELECTRONICS: YOU MIGHT FIND THE RECENT ISSUE OF - - -

PUT ON BOARD:

PROJECT STATUS AS OF NOON, TUES. NOV 28:

1. COMPLETE/NEARLY COMPLETE. HAVE SEEN PLOT.  
FINAL DIMENSIONS KNOWN. COMMITTED TO GO ON  
CHIP:

✓✓ LOGO MIT.CIF  
✓✓ STEELE (STEELE.CIF)  
✓✓ CHERRY PROJ.CIF  
✓✓ OTTEN CLOCKMASTER  
✓✓ LAM LAM.CIF  
✓✓ STERN PROJ.CIF  
✓✓ FRANK WPLA.CIF  
✓✓ YANG SORTER.CIF  
✓✓ COLN ~~D.A.~~ A.CIF  
✓✓ ROYLANCE ZZZZZZ.CIF  
✓✓ LEVITT F.F  
~~PEREA BYTELSI~~  
✓✓ PEREA BYTELSI  
✓✓ BROCK, BOUGHTON, BRYANT, LEUNG (DATA MANIP) BROCK.CIF  
✓✓ HIRATSUKA ~~COMPAR.CIF~~ HIRATSUKA.CIF

2. NEARLY COMPLETE. APPROX DIMENSIONS KNOWN.  
LIKELY TO GET ON CHIP, IF FINISH IN TIME.

✓ WOLSON OLSON.CIF  
✓✓ SNYDER (SNYDER.CIF)  
✓✓ SHAVER MFM.CIF  
✓✓ BAIN PROJECT  
✓✓ BALDWIN NTS.CIF  
✓✓ WESTBROOK, GOLDIKENER (E-TESTS) ~~STRENGTH~~ <ST.GOLDIKENER> ROUGH

3. WORK IN PROGRESS. MIGHT GET ON CHIP

~~FRANKEL~~  
✓✓ FRANKEL .PROJECT.CIF  
RUBENSTEIN, AZOURY, BOWEN (CFT PROJ.)  
✓✓ FRANKEL (FRANKEL.CIF) -

4. INSUFF. INFO: ✓✓ RUBENSTEIN, AZOURY, BOWEN (CRT CONT.) <ST.RUBENSTEIN> CRT  
REYNOLDS  
MCLELLAN

W = \* SENT TO PARC

- TODAY I'M VERY PLEASSED TO INTRODUCE  
RICK DAVIES, MEMBER OF THE LBS. STP, XEROX PARC,

RICK WILL BE GIVING A SEMINAR ENTITLED:

I THINK YOU WILL FIND THIS MATERIAL QUITE INTERESTING:  
WE'LL BE GOING BACK AND TAKING A CLOSER  
LOOK AT THE FET, AND HOW IT WORKS,

AND ALSO AT WAYS OF EXTRACTING  
SIMULATION PARAMETERS FROM ACTUAL  
MEASUREMENT ON WAFER, SO AS TO  
PROVIDE <sup>BETTER</sup> "CALIBRATED" SIMULATIONS.  
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