Memo from: LYNN CONWAY

SU SUMMARY:

19 proj.	79.16 mm²

+ HIT LIST:

5 proj. 28.66 mm²

3

XEROX

SU FINAL

Summary of designs from SU, updated 4-Dec-79 20:40:30

✓ AtlasSU

Designer(s): Les Atlas, Doug Galbraith
Description: This project is an neural-stim. interval timer
Est.BB: ~ 2500 x 2000 microns. *

Space is allocated.
Reserved space = 2478 x 1378 microns, Area = 3.41 sq mm
Priority time: 30-Nov-79 23:42:48
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>ATLASSU.CIF;4
File creation date: 4-Dec-79 16:28:09
Bounding box = 2478 x 1378 microns, Area = 3.41 sq mm

✓ BasкettSU

Designer(s): Forest Baskett
Description: This project is an Ethernet synchronizer
Est.BB: ~ 2250 X 2500 microns.

Space is allocated.
Reserved space = 2240 x 2720 microns, Area = 6.09 sq mm
Priority time: 26-Nov-79 8:52:03
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>BASKETTSU.CIF;1
File creation date: 26-Nov-79 8:52:03
Bounding box = 2240 x 2720 microns, Area = 6.09 sq mm

✓ BectolsheimSU

Designer(s): Andy Bechtolsheim, Thomas Gross
Description: This project is a parallel search table for log arithmetic
Est.BB: ~ 3500 X 1800 microns.

Space is allocated.
Reserved space = 1514 x 3180 microns, Area = 4.81 sq mm
Priority time: 23-Nov-79 15:41:04
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>BECTOLSHEIMSU.CIF;3
File creation date: 3-Dec-79 22:52:06
Bounding box = 1514 x 3180 microns, Area = 4.81 sq mm

✓ Clark2SU

Designer(s): Jim Clark
Description: This project is a self-timed clock element
Est BB: ~ 1200 x 1200 microns.

Design is awaiting allocation.
Required space = 1606 x 1688 microns, Area = 2.71 sq mm
Priority time: 1-Dec-79 19:02:59
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>CLARK2SU.CIF;3
File creation date: 4-Dec-79 13:21:54
Bounding box = 1606 x 1688 microns, Area = 2.71 sq mm

✓ ClarkSU

Designer(s): Jim Clark
Description: This project is a simple graphics ALU
Est.BB: ~ 3000 x 3000 microns

Space is allocated.
Reserved space = 2976 x 2764 microns, Area = 8.23 sq mm
Priority time: 28-Nov-79 14:57:42
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>CLARKSU.CIF;3
File creation date: 4-Dec-79 16:18:37
Bounding box = 2976 x 2764 microns, Area = 8.23 sq mm

ElahianSU

Designer(s): Kamran Elahian, Fred Basham
Description: This project is a UART line speed determiner
Est.BB: ~ 1950 X 1900 microns.

Space is allocated.
Reserved space = 1856 x 1856 microns, Area = 3.44 sq mm
Priority time: 1-Dec-79 16:11:10
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>ELAHIANSU.CIF;3
File creation date: 4-Dec-79 15:34:00
Bounding box = 1856 x 1856 microns, Area = 3.44 sq mm

FrolikSU

Designer(s): Bill Frolik, Roderick Young
Description: This project is a digital timer
Est.BB: ~ 2750 x 2125 microns. *

Design is awaiting allocation.
Required space = 2120 x 2684 microns, Area = 5.69 sq mm
Priority time: 4-Dec-79 12:08:07
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>FROLIKSU.CIF;5
File creation date: 4-Dec-79 12:08:07
Bounding box = 2120 x 2684 microns, Area = 5.69 sq mm

GehlbachSU

Designer(s): Steve Gehlbach, Joe Sharp, Bill Jansen
Description: This project is a fast 16-input adder
Est.BB: ~ 1250 X 3250 microns. *

Space is allocated.
Reserved space = 3180 x 1856 microns, Area = 5.90 sq mm
Priority time: 30-Nov-79 8:36:06
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>GEHLBACHSU.CIF;6
File creation date: 4-Dec-79 11:49:07
Bounding box = 3180 x 1856 microns, Area = 5.90 sq mm

HannahSU

Designer(s): Peter Eichenberger, Marc Hannah
Description: This project is a rectangle generator
Est.BB: ~ 2000 X 2500 microns.

Space is allocated.
Reserved space = 2386 x 2140 microns, Area = 5.11 sq mm
Priority time: 30-Nov-79 21:33:31
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>HANNAHSU.CIF;6
File creation date: 4-Dec-79 12:09:11
Bounding box = 2386 x 2140 microns, Area = 5.11 sq mm

HerndonSU

Designer(s): Matt Herndon, Jeff Thorson
Description: This project is a typesetting machine
Est.BB: ~ 2500 X 2250 microns.

Space is allocated.
Reserved space = 3170 x 2000 microns, Area = 6.34 sq mm
Priority time: 30-Nov-79 23:45:42
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>HERNDONSU.CIF;2
File creation date: 3-Dec-79 22:42:50
Bounding box = 3170 x 2000 microns, Area = 6.34 sq mm

✔ MacomberSU

Designer(s): Scott Macomber, Bob Clark
Description: This project is a parallel/serial multiplier
Est.BB: ~ 1900 X 1900 microns.

Space is allocated.
Reserved space = 2000 x 2000 microns, Area = 4.00 sq mm
Priority time: 2-Dec-79 22:18:27
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>MACOMBERSU.CIF;4
File creation date: 4-Dec-79 11:50:29
Bounding box = 2000 x 2000 microns, Area = 4.00 sq mm

✔ MarkeeSU

Designer(s): Pat Markee, Irene Watson
Description: This project is a digital clock
Est.BB: ~ 2000 X 3000 microns.

Space is allocated.
Reserved space = 2120 x 1424 microns, Area = 3.02 sq mm
Priority time: 30-Nov-79 21:52:42
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>MARKEESU.CIF;2
File creation date: 4-Dec-79 15:38:53
Bounding box = 2120 x 1424 microns, Area = 3.02 sq mm

✔ MathewsSU

Designer(s): Rob Mathews, John Newkirk
Desciption: This project is the infamous Buffalo chip
Est.BB: ~ 5000 X 1250 microns.

Space is allocated.
Reserved space = 5180 x 1134 microns, Area = 5.87 sq mm
Priority time: 23-Nov-79 15:33:45
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>MATHEWSSU.CIF;1
File creation date: 23-Nov-79 15:33:45
Bounding box = 5180 x 1134 microns, Area = 5.87 sq mm

✔ NoiceSU

Designer(s): David Noice, Neil Midkiff
Description: This project is a multiplier/divider
Est.BB: ~ 2750 X 1500 microns.

Space is allocated.
Reserved space = 2888 x 1576 microns, Area = 4.55 sq mm
Priority time: 1-Dec-79 16:14:51
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>NOICESU.CIF;2
File creation date: 4-Dec-79 15:20:56
Bounding box = 2888 x 1576 microns, Area = 4.55 sq mm

✔ OhChinSU

Designer(s): Soo-Young Oh, Dae-Je Chin
Description: This project is a automatic thermostat time controler
Est.BB: ~ 2150 X 1600 microns.

Space is allocated.
Reserved space = 2120 x 1700 microns, Area = 3.60 sq mm
Priority time: 30-Nov-79 8:33:40
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>OHCHINSU.CIF;3
File creation date: 4-Dec-79 9:02:42
Bounding box = 2120 x 1700 microns, Area = 3.60 sq mm

OyeSU HIT

Designer(s): Kevin Oye, Alan Siegel
Description: This project is a error-correcting parallel/serial interface
Est.BB: ~ 3000 X 2500 microns.

Design is awaiting allocation.
Required space = 1882 x 1486 microns, Area = 2.80 sq mm
Priority time: 4-Dec-79 17:00:38
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>OYESU.CIF;2
File creation date: 4-Dec-79 17:00:38
Bounding box = 1882 x 1486 microns, Area = 2.80 sq mm

StrongSU HIT

Designer(s): Alex Strong, Danny Sleator
Description: This project is a guitar chip
Est.BB: ~ 3000 X 1875 microns.

Design is awaiting allocation.
Required space = 1856 x 2120 microns, Area = 3.93 sq mm
Priority time: 4-Dec-79 17:03:35
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>STRONGSU.CIF;4
File creation date: 4-Dec-79 17:03:35
Bounding box = 1856 x 2120 microns, Area = 3.93 sq mm

SytwuSU HIT

Designer(s): J. Sytwu, Hamid Najafi
Description: This project is a quad PCM bus interface
Est.BB: ~ 3000 X 3000 microns.

Space is allocated.
Reserved space = 4150 x 3146 microns, Area = 13.06 sq mm
Priority time: 1-Dec-79 12:36:07
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>SYTWUSU.CIF;2
File creation date: 4-Dec-79 16:55:21
Bounding box = 4150 x 3146 microns, Area = 13.06 sq mm

✓ TarsiSU HIT

Designer(s): Mike Tarsi, Nagatsugu Yamanouchi
Description: This project is a multifunction digital clock
Est.BB: ~ 1920 X 1920 microns.

Design is awaiting allocation.
Required space = 2140 x 2276 microns, Area = 4.87 sq mm
Priority time: 1-Dec-79 12:56:09
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>TARSISU.CIF;3
File creation date: 4-Dec-79 12:10:50
Bounding box = 2140 x 2276 microns, Area = 4.87 sq mm

✓ UttSU HIT

Designer(s): Steve Utt, Shalom Ackelsberg
Description: This project is part of a pancreas prosthesis
Est B.B.: 2000 X 2000 microns

Space is allocated.
Reserved space = 2000 x 2000 microns, Area = 4.00 sq mm
Priority time: 1-Dec-79 12:56:46
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>UTTSU.CIF;3
File creation date: 4-Dec-79 11:55:45
Bounding box = 2000 x 2000 microns, Area = 4.00 sq mm

✓ WulffSU

Designer(s): Bob Wulff, Tom Bennett
Description: This project is a bit slice of a multiplier
Est.BB: ~ 2375 X 2125 microns.

Design is awaiting allocation.
Required space = 2120 x 1856 microns, Area = 3.93 sq mm
Priority time: 3-Dec-79 20:31:30
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>WULFFSU.CIF;3
File creation date: 4-Dec-79 12:12:38
Bounding box = 2120 x 1856 microns, Area = 3.93 sq mm

✓ ZarghanSU

Designer(s): Bahman Zargham, Jerry Huck
Description: This project is a multiplexed communications link
Est.BB: ~ 2250 X 1900 microns.

Space is allocated.
Reserved space = 1590 x 1550 microns, Area = 2.46 sq mm
Priority time: 30-Nov-79 21:36:20
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>ZARGHANSU.CIF;4
File creation date: 3-Dec-79 23:03:11
Bounding box = 1590 x 1550 microns, Area = 2.46 sq mm

SU

Summary of designs from SU, updated 4-Dec-79 3:51:33

AhmedSU

Designer(s): Hassan Ahmed, Rich Baker
Description: This project is a forward error-correction codec
Est.BB: ~ 1250 X 2250 microns.

Design is not ready for space allocation.
No file has been submitted for implementation.

AtlasSU

Designer(s): Les Atlas, Doug Galbraith
Description: This project is an neural-stim. interval timer
Est.BB: ~ 2500 x 2000 microns. *

Space is allocated.
Reserved space = 2478 x 1378 microns, Area = 3.41 sq mm
Priority time: 30-Nov-79 23:42:48
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>ATLASSU.CIF;3
File creation date: 3-Dec-79 22:37:44
Bounding box = 2478 x 1378 microns, Area = 3.41 sq mm

BaskettSU

Designer(s): Forest Baskett
Description: This project is an Ethernet synchronizer
Est.BB: ~ 2250 X 2500 microns.

Space is allocated.
Reserved space = 2240 x 2720 microns, Area = 6.09 sq mm
Priority time: 26-Nov-79 8:52:03
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>BASKETTSU.CIF;1
File creation date: 26-Nov-79 8:52:03
Bounding box = 2240 x 2720 microns, Area = 6.09 sq mm

BectolsheimSU

Designer(s): Andy Bechtolsheim, Thomas Gross
Description: This project is a parallel search table for log arithmetic
Est.BB: ~ 3500 X 1800 microns.

Space is allocated.
Reserved space = 1514 x 3180 microns, Area = 4.81 sq mm
Priority time: 23-Nov-79 15:41:04
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>BECTOLSHEIMSU.CIF;3
File creation date: 3-Dec-79 22:52:06
Bounding box = 1514 x 3180 microns, Area = 4.81 sq mm

Clark2SU

Designer(s): Jim Clark
Description: This project is a self-timed clock element
Est BB: ~ 1200 x 1200 microns.

Space is allocated.
Reserved space = 1630 x 1642 microns, Area = 2.68 sq mm
Priority time: 1-Dec-79 19:02:59
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>CLARK2SU.CIF;2
File creation date: 3-Dec-79 0:09:46
Bounding box = 1630 x 1642 microns, Area = 2.68 sq mm

ClarkSU

Designer(s): Jim Clark

Description: This project is a simple graphics ALU
Est.BB: ~ 3000 x 3000 microns

Space is allocated.
Reserved space = 2764 x 2976 microns, Area = 8.23 sq mm
Priority time: 28-Nov-79 14:57:42
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>CLARKSU.CIF;1
File creation date: 28-Nov-79 14:57:42
Bounding box = 2764 x 2976 microns, Area = 8.23 sq mm

ElahianSU

Designer(s): Kamran Elahian, Fred Basham
Description: This project is a UART line speed determiner
Est.BB: ~ 1950 X 1900 microns.

Space is allocated.
Reserved space = 1856 x 1856 microns, Area = 3.44 sq mm
Priority time: 1-Dec-79 16:11:10
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>ELAHIANSU.CIF;2
File creation date: 3-Dec-79 22:59:42
Bounding box = 1856 x 1856 microns, Area = 3.44 sq mm

ErbilSU

Designer(s): Oktay Erbil, Peter Fu
Description: This project is a consistency unit for a fault-tolerant system
Est.BB: ~ 2250 X 1950 microns.

Space is allocated.
Reserved space = 2250 x 2500 microns, Area = 5.63 sq mm
Priority time: 2-Dec-79 22:17:37
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>ERBILSU.CIF;2
File creation date: 2-Dec-79 22:17:37
Bounding box = 2250 x 2500 microns, Area = 5.63 sq mm

FrolikSU

Designer(s): Bill Frolik, Roderick Young
Description: This project is a digital timer
Est.BB: ~ 2750 x 2125 microns. *

Design is not ready for space allocation.
Current submittal is not implementable.
File name: [Maxc]<SU-VLSI>FROLIKSU.CIF;4
File creation date: 1-Dec-79 15:49:41

GehlbachSU

Designer(s): Steve Gehlbach, Joe Sharp, Bill Jansen
Description: This project is a fast 16-input adder
Est.BB: ~ 1250 X 3250 microns. *

Space is allocated.
Reserved space = 3180 x 1856 microns, Area = 5.90 sq mm
Priority time: 30-Nov-79 8:36:06
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>GEHLBACHSU.CIF;5
File creation date: 3-Dec-79 9:30:45
Bounding box = 3180 x 1856 microns, Area = 5.90 sq mm

GlussSU

Designer(s): Dave Gluss, Bill Nowicki
Description: This project is a Ethernet deserializing buffer
Est.BB: ~ 5000 X 1250 microns.

Space is allocated.

Reserved space = 3180 x 1644 microns, Area = 5.23 sq mm
Priority time: 1-Dec-79 12:31:33
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>GLUSSSU.CIF;1
File creation date: 1-Dec-79 12:31:33
Bounding box = 3180 x 1644 microns, Area = 5.23 sq mm

HannahSU

Designer(s): Peter Eichenberger, Marc Hannah
Description: This project is a rectangle generator
Est.BB: ~ 2000 X 2500 microns.

Space is allocated.
Reserved space = 2386 x 2140 microns, Area = 5.11 sq mm
Priority time: 30-Nov-79 21:33:31
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>HANNAHSU.CIF;6
File creation date: 3-Dec-79 16:43:58
Bounding box = 2386 x 2140 microns, Area = 5.11 sq mm

HerndonSU

Designer(s): Matt Herndon, Jeff Thorson
Description: This project is a typesetting machine
Est.BB: ~ 2500 X 2250 microns.

Space is allocated.
Reserved space = 3170 x 2000 microns, Area = 6.34 sq mm
Priority time: 30-Nov-79 23:45:42
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>HERNDONSU.CIF;2
File creation date: 3-Dec-79 22:42:50
Bounding box = 3170 x 2000 microns, Area = 6.34 sq mm

HorowitzSU

Designer(s): Mark Horowitz, Wayne Wolf
Description: This project is a model train speed controller
Est.BB: ~ 2000 X 2125 microns.

Design is awaiting allocation.
Required space = 2430 x 2160 microns, Area = 5.25 sq mm
Priority time: 3-Dec-79 20:29:55
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>HOROWITZSU.CIF;2
File creation date: 3-Dec-79 20:29:55
Bounding box = 2430 x 2160 microns, Area = 5.25 sq mm

HuangSU

Designer(s): Wen-her Huang
Description: This project is a CAM
Est.BB: ~ 2000 X 2000 microns.

Design is awaiting allocation.
Required space = 2100 x 2000 microns, Area = 4.20 sq mm
Priority time: 3-Dec-79 16:45:54
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>HUANGSU.CIF;1
File creation date: 3-Dec-79 16:45:54
Bounding box = 2100 x 2000 microns, Area = 4.20 sq mm

KarmarkarSU

Designer(s): Narendra Karmarkar, Timothy Gonsalves
Description: This project is a bit-slice residue autocorrelator
Est.BB: ~ 2000 X 2125 microns.

Design is not ready for space allocation.
No file has been submitted for implementation.

MacomberSU

Designer(s): Scott Macomber, Bob Clark
Description: This project is a parallel/serial multiplier
Est.BB: ~ 1900 X 1900 microns.

Space is allocated.
Reserved space = 2000 x 2000 microns, Area = 4.00 sq mm
Priority time: 2-Dec-79 22:18:27
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>MACOMBERSU.CIF;3
File creation date: 2-Dec-79 22:18:27
Bounding box = 2000 x 2000 microns, Area = 4.00 sq mm

MarkeeSU

Designer(s): Pat Markee, Irene Watson
Description: This project is a digital clock
Est.BB: ~ 2000 X 3000 microns.

Space is allocated.
Reserved space = 2120 x 1424 microns, Area = 3.02 sq mm
Priority time: 30-Nov-79 21:52:42
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>MARKEESU.CIF;1
File creation date: 30-Nov-79 21:52:42
Bounding box = 2120 x 1424 microns, Area = 3.02 sq mm

MathewsSU

Designer(s): Rob Mathews, John Newkirk
Desciption: This project is the infamous Buffalo chip
Est.BB: ~ 5000 X 1250 microns.

Space is allocated.
Reserved space = 5180 x 1134 microns, Area = 5.87 sq mm
Priority time: 23-Nov-79 15:33:45
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>MATHEWSSU.CIF;1
File creation date: 23-Nov-79 15:33:45
Bounding box = 5180 x 1134 microns, Area = 5.87 sq mm

NoiceSU

Designer(s): David Noice, Neil Midkiff
Description: This project is a multiplier/divider
Est.BB: ~ 2750 X 1500 microns.

Space is allocated.
Reserved space = 2888 x 1576 microns, Area = 4.55 sq mm
Priority time: 1-Dec-79 16:14:51
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>NOICESU.CIF;1
File creation date: 1-Dec-79 16:14:51
Bounding box = 2888 x 1576 microns, Area = 4.55 sq mm

OhChinSU

Designer(s): Soo-Young Oh, Dae-Je Chin
Description: This project is a automatic thermostat time controler
Est.BB: ~ 2150 X 1600 microns.

Space is allocated.
Reserved space = 2120 x 1700 microns, Area = 3.60 sq mm
Priority time: 30-Nov-79 8:33:40
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>OHCHINSU.CIF;2
File creation date: 30-Nov-79 8:33:40
Bounding box = 2120 x 1700 microns, Area = 3.60 sq mm

OyeSU

Designer(s): Kevin Oye, Alan Siegel
Description: This project is a error-correcting parallel/serial interface
Est.BB: ~ 3000 X 2500 microns.

Design is not ready for space allocation.
No file has been submitted for implementation.

RedfordSU

Designer(s): John Redford, Lyle Smith
Description: This project is a self test memory
Est.BB: ~ 3125 x 2875 microns

Space is allocated.
Reserved space = 2126 x 2776 microns, Area = 5.90 sq mm
Priority time: 1-Dec-79 12:33:37
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>REDFORDSU.CIF;1
File creation date: 1-Dec-79 12:33:37
Bounding box = 2126 x 2776 microns, Area = 5.90 sq mm

StrongSU

Designer(s): Alex Strong, Danny Sleator
Description: This project is a guitar chip
Est.BB: ~ 3000 X 1875 microns.

Design is not ready for space allocation.
Current submittal is not implementable.
File name: [Maxc]<SU-VLSI>STRONGSU.CIF;2
File creation date: 1-Dec-79 15:45:03

SytwuSU

Designer(s): J. Sytwu, Hamid Najafi
Description: This project is a quad PCM bus interface
Est.BB: ~ 3000 X 3000 microns.

Space is allocated.
Reserved space = 3000 x 2546 microns, Area = 7.64 sq mm
Priority time: 1-Dec-79 12:36:07
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>SYTWUSU.CIF;1
File creation date: 1-Dec-79 12:36:07
Bounding box = 3000 x 2546 microns, Area = 7.64 sq mm

TarsiSU

Designer(s): Mike Tarsi, Nagatsugu Yamanouchi
Description: This project is a multifunction digital clock
Est.BB: ~ 1920 X 1920 microns.

Design is awaiting allocation.
Required space = 2140 x 2276 microns, Area = 4.87 sq mm
Priority time: 1-Dec-79 12:56:09
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>TARSISU.CIF;2
File creation date: 3-Dec-79 16:46:34
Bounding box = 2140 x 2276 microns, Area = 4.87 sq mm

UttSU

Designer(s): Steve Utt, Shalom Ackelsberg
Description: This project is part of a pancreas prosthesis
Est B.B.: 2000 X 2000 microns

Space is allocated.
Reserved space = 2000 x 2000 microns, Area = 4.00 sq mm

Priority time: 1-Dec-79 12:56:46
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>UTTSU.CIF;2
File creation date: 1-Dec-79 12:56:46
Bounding box = 2000 x 2000 microns, Area = 4.00 sq mm

WulffSU

Designer(s): Bob Wulff, Tom Bennett
Description: This project is a bit slice of a multiplier
Est.BB: ~ 2375 X 2125 microns.

Design is awaiting allocation.
Required space = 2120 x 1856 microns, Area = 3.93 sq mm
Priority time: 3-Dec-79 20:31:30
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>WULFFSU.CIF;2
File creation date: 3-Dec-79 20:31:30
Bounding box = 2120 x 1856 microns, Area = 3.93 sq mm

ZarghanSU

Designer(s): Bahman Zargham, Jerry Huck
Description: This project is a multiplexed communications link
Est.BB: ~ 2250 X 1900 microns.

Space is allocated.
Reserved space = 1590 x 1550 microns, Area = 2.46 sq mm
Priority time: 30-Nov-79 21:36:20
Current submittal is acceptable for implementation.
File name: [Maxc]<SU-VLSI>ZARGHANSU.CIF;4
File creation date: 3-Dec-79 23:03:11
Bounding box = 1590 x 1550 microns, Area = 2.46 sq mm

SU Priorities

(1) M. Trews, Baskett, Bechtolsheim, Clark

(2) Atlas, Herndon, Noire, OhChin, Frolik, Hannah, Gehlbach, Macomber, Elahian, Wulff, Zarghan

(3) Markee, Utt, Tarpi

(4) Sytwu

(5) Oye, Strong

(494-2959)

HIT