Memo from: LYNN CONWAY

U of R Summary

5 proj.	30.34 mm^2

XEROX

UofR FINAL

Summary of designs from UofR, updated 4-Dec-79 23:13:17

✔ KedemUR

Designers(s): Gershon Kedem and Michel Denber
Description: Infinite precision multiplier
Est.BB: ~ 2000 x 2000 microns.

Design is awaiting allocation.
Required space = 2698 x 2786 microns, Area = 7.52 sq mm
Priority time: 4-Dec-79 20:06:31
Current submittal is acceptable for implementation.
File name: [Maxc]<LYON>KEDEMUR.CIF;1
File creation date: 4-Dec-79 20:06:31
Bounding box = 2698 x 2786 microns, Area = 7.52 sq mm

✔ LyonsUR

Implementer: Bob Lyons
Description: Programmable Frequency Generator
Est.BB: 3000 microns x 3000 microns

Design is awaiting allocation.
Required space = 2748 x 2276 microns, Area = 6.25 sq mm
Priority time: 4-Dec-79 20:04:24
Current submittal is acceptable for implementation.
File name: [Maxc]<UOFR-VLSI>LYONSUR.CIF;2
File creation date: 4-Dec-79 20:04:24
Bounding box = 2748 x 2276 microns, Area = 6.25 sq mm

✔ SohmUR

Designers: Larry Sohm, Pat Chan, Bill Notowitz
Description: Digital Phase lock loop
Est.BB: 1500 x 3000 microns.

Design is awaiting allocation.
Required space = 3610 x 2634 microns, Area = 9.51 sq mm
Priority time: 4-Dec-79 19:06:24
Current submittal is acceptable for implementation.
File name: [Maxc]<UOFR-VLSI>SOHMUR.CIF;2
File creation date: 4-Dec-79 19:06:24
Bounding box = 3610 x 2634 microns, Area = 9.51 sq mm

✔ TiloveUR

Designer(s): Bob Tilove, Jarek Rossignac
Description: This is a bit slice coordinate transformer
Est. BB: ~ 1400 X 2000 microns.

Design is awaiting allocation.
Required space = 1934 x 1326 microns, Area = 2.56 sq mm
Priority time: 4-Dec-79 18:39:39
Current submittal is acceptable for implementation.
File name: [Maxc]<LYON>TILOVEUR.CIF;1
File creation date: 4-Dec-79 18:39:39
Bounding box = 1934 x 1326 microns, Area = 2.56 sq mm

✔ WatanabeUR

Designer: Yuki Watanabe
Description: Sorter slice
Est.BB: 2300 x 2600 micron

Design is awaiting allocation.
Required space = 2008 x 2240 microns, Area = 4.50 sq mm
Priority time: 4-Dec-79 19:09:22
Current submittal is acceptable for implementation.
File name: [Maxc]<UOFR-VLSI>WATANABEUR.CIF;2
File creation date: 4-Dec-79 19:09:22
Bounding box = 2008 x 2240 microns, Area = 4.50 sq mm

UofR

Summary of designs from UofR, updated 4-Dec-79 3:51:33

ErmerUR

Implementer: Rick Ermer and Tuan Nguyen
Description: Speedometer/Odometer
Est.BB: 2000 microns x 3750 microns

Design is not ready for space allocation.
No file has been submitted for implementation.

KahrsUR

Designer: Mark Kahrs
Description: 13 bit DAC
Est.BB: 2500 x 1500 microns

Design is not ready for space allocation.
No file has been submitted for implementation.

KedemUR

Designers(s): Gershon Kedem and Michel Denber
Description: Infinite precision multiplier
Est.BB: ~ 2000 x 2000 microns.

Design is not ready for space allocation.
No file has been submitted for implementation.

LylesUR

Implementer: Brian Lyles
Description: Interpolative A/D
Est.BB: 2000 microns x 1250 microns

Design is not ready for space allocation.
No file has been submitted for implementation.

LyonsUR

Implementer: Bob Lyons
Description: Programmable Frequency Generator
Est.BB: 3000 microns x 3000 microns

Design is not ready for space allocation.
No file has been submitted for implementation.

RajUR

Implementer: Raj
Description: CAM
Est.BB: 2000 microns x 500 microns

Design is not ready for space allocation.
No file has been submitted for implementation.

SabbahUR

Designer: Danny Sabbah
Description: Programmable SLA
Est.BB: ~ 4600 x 5500 microns

Design is not ready for space allocation.
No file has been submitted for implementation.

SohmUR

Designers: Larry Sohm, Pat Chan, Bill Notowitz

Description: Digital Phase lock loop
Est.BB: 1500 x 3000 microns.

Design is not ready for space allocation.
No file has been submitted for implementation.

TiloveUR

Designer(s): Bob Tilove, Jarek Rossignac
Description: This is a bit slice coordinate transformer
Est. BB: ~ 1400 X 2000 microns.

Design is not ready for space allocation.
No file has been submitted for implementation.

UofR Priority:

- Watanabe
- Kedem — ON [maxc]<lyon>
- Lyons
- Sohm

- TiloverUR.CIF — on [Maxc]<lyon>

Sabbah — Some unknown. Flagrantly wasteful of space

Kahrs	N.G.
Lyles	N.G.
Raj	N.G.