Memo from: LYNN CONWAY

MIT

SUMMARY:

~~15~~ 14 proj.	91.23 mm²

XEROX

MIT FINAL

Summary of designs from MIT, updated 4-Dec-79 19:02:23

AllenMIT

Designers: Don Allen, Jerry Burchfiel
Description: Variable Length Field Decoder
Est.BB: 2500 x 2500 microns

Space is allocated.
Reserved space = 2218 x 2484 microns, Area = 5.51 sq mm
Priority time: 28-Nov-79 7:45:20
Current submittal is acceptable for implementation.
File name: [Maxc]<ABELL>ALLENMIT.CIF;3
File creation date: 4-Dec-79 18:44:50
Bounding box = 2218 x 2484 microns, Area = 5.51 sq mm

BataliMIT

Designers: John Batali
Description: Zero-Crossing Detector for Image Processing
Est.BB 2650 x 1575

Design is awaiting allocation.
Required space = 2644 x 1738 microns, Area = 4.60 sq mm
Priority time: 29-Nov-79 5:59:14
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>BATALIMIT.CIF;3
File creation date: 4-Dec-79 16:35:08
Bounding box = 2644 x 1738 microns, Area = 4.60 sq mm

BodonyMIT

Designers: Larry Bodony, Bruce Rose
Description: Logic State Analyzer
Est.BB: 4500 x 2650 microns

Design is awaiting allocation.
Required space = 4500 x 4336 microns, Area = 19.51 sq mm
Priority time: 28-Nov-79 7:45:43
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>BODONYMIT.CIF;2
File creation date: 4-Dec-79 16:55:27
Bounding box = 4500 x 4336 microns, Area = 19.51 sq mm

ChangMIT

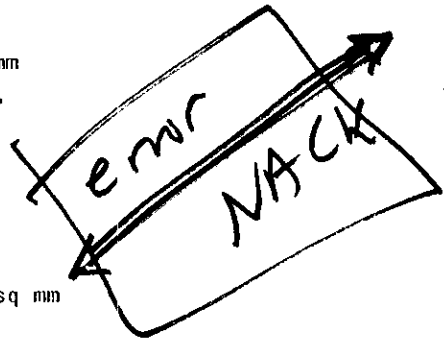
Designers: Frank Chang, Doug Williams
Description: Error-detecting block transfer oriented channel interface
Est.BB: 2500 x 5250 microns

Space is allocated.
Reserved space = 2764 x 2500 microns, Area = 6.91 sq mm
Priority time: 29-Nov-79 5:59:33
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>CHANGMIT.CIF;4
File creation date: 4-Dec-79 16:17:37
Bounding box = 2764 x 2500 microns, Area = 6.91 sq mm

ChuMIT

Designers: Tam-Anh Chu, Nhi-Anh Chu, Steve McCormick
Description: Second order digital filter stage
Est.BB: 2400 x 6200 microns

Space is allocated.
Reserved space = 6146 x 2278 microns, Area = 14.00 sq mm
Priority time: 29-Nov-79 6:00:45
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>CHUMIT.CIF;3
File creation date: 4-Dec-79 13:47:06
Bounding box = 6146 x 2278 microns, Area = 14.00 sq mm



RANDY SAYS THIS CAN'T BE RIGHT (TOO BIG!)

? TRY TO LOOK AT THIS

FichtenbaumMIT

Designers:Matt Fichtenbaum
Description: A digital pulse rate monitor
Est.BB: 2500 x 2500 microns

Space is allocated.
Reserved space = 2500 x 2500 microns, Area = 6.25 sq mm
Priority time: 29-Nov-79 15:05:20
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>FICHTENBAUMMIT.CIF;2
File creation date: 4-Dec-79 14:07:50
Bounding box = 2500 x 2500 microns, Area = 6.25 sq mm

GoddeauMIT

Designers:David Goddeau, Jonathan Sieber, Chris Terman
Description: A first-in, priority-out buffer
Est.BB:3000 x 3000 microns

Space is allocated.
Reserved space = 2928 x 2954 microns, Area = 8.65 sq mm
Priority time: 28-Nov-79 15:16:15
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>GODDEAUMIT.CIF;2
File creation date: 4-Dec-79 0:07:02
Bounding box = 2928 x 2954 microns, Area = 8.65 sq mm

GoodrichMIT

Designers: Earl Goodrich
Description: CRT controller
Est.BB: 2000 x 1600 microns

Space is allocated.
Reserved space = 1856 x 1520 microns, Area = 2.82 sq mm
Priority time: 1-Dec-79 20:52:01
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>GOODRICHMIT.CIF;2
File creation date: 4-Dec-79 12:49:03
Bounding box = 1856 x 1520 microns, Area = 2.82 sq mm

GramlichMIT

Designers: Wayne Gramlich, Carl Seaquist
Description: A writable PLA in which the programming of the AND and
OR planes is defined by contents of the static RAM cells.
Also cam program feedback loops to form finite state machines.
Est.BB: 2200 X 1700 microns.

Design is awaiting allocation.
Required space = 1524 x 1906 microns, Area = 2.90 sq mm
Priority time: 27-Nov-79 10:13:36
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>GRAMLICHMIT.CIF;2
File creation date: 29-Nov-79 19:35:37
Bounding box = 1524 x 1906 microns, Area = 2.90 sq mm

GrondalskiMIT

Designers: Robert Grondalski
Description: Writeable PLA
Est.BB: 2200 x 2200 microns

Space is allocated.
Reserved space = 2200 x 2200 microns, Area = 4.84 sq mm
Priority time: 28-Nov-79 7:47:36
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>GRONDALSKIMIT.CIF;2
File creation date: 4-Dec-79 13:27:20

Bounding box = 2200 x 2200 microns, Area = 4.84 sq mm

✔HamiltonMIT

Designers:Brian Hamilton
Description: Digital Alarm Clock
Est.BB: 2500 x 2500 microns

Space is allocated.
Reserved space = 2500 x 2500 microns, Area = 6.25 sq mm
Priority time: 1-Dec-79 11:25:05
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>HAMILTONMIT.CIF;2
File creation date: 4-Dec-79 16:18:21
Bounding box = 2500 x 2500 microns, Area = 6.25 sq mm

✔KathailMIT

Designers: Vinod Kathail, Keshav Pingali
Description: an interpreter for mapping programs onto a data flow computer
Est.BB: 2250 x 1750 microns

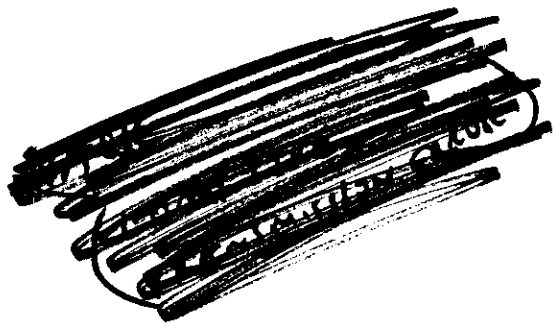
Design is awaiting allocation.
Required space = 1590 x 2228 microns, Area = 3.54 sq mm
Priority time: 29-Nov-79 6:01:43
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>KATHAILMIT.CIF;3
File creation date: 4-Dec-79 15:28:28
Bounding box = 1590 x 2228 microns, Area = 3.54 sq mm

✔KhouryMIT

Designers: John Khoury
Description: Up-Down counter with programmable modulus
Est.BB: 2000 x 1725 microns

Space is allocated.
Reserved space = 2000 x 1726 microns, Area = 3.45 sq mm
Priority time: 29-Nov-79 15:06:27
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>KHOURYMIT.CIF;2
File creation date: 4-Dec-79 13:26:17
Bounding box = 2000 x 1726 microns, Area = 3.45 sq mm

✔PasemanMIT



Designers: Bill Paseman
Description: Music Synthesizer
Est.BB:4250 x 1750 microns

Space is allocated.
Reserved space = 4438 x 2944 microns, Area = 13.07 sq mm
Priority time: 30-Nov-79 13:07:25
Current submittal is not implementable.
File name: [Maxc]<MIT-VLSI>PASEMANMIT.CIF;2
File creation date: 4-Dec-79 10:17:59

✔PicardMIT

Designers: Len Picard
Description: Variable format field extractor and compactor
Est.BB: 2000 x 1750 microns

Space is allocated.
Reserved space = 2000 x 1688 microns, Area = 3.38 sq mm
Priority time: 29-Nov-79 6:02:08
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>PICARDMIT.CIF;3
File creation date: 4-Dec-79 12:50:38
Bounding box = 2000 x 1688 microns, Area = 3.38 sq mm

RivestMIT

Designers: Ron Rivest, Len Adleman, Adi Shamir
Description: Section of a Multiplier
Est.BB: 2000 x 2000

Space is allocated.
Reserved space = 2250 x 2250 microns, Area = 5.06 sq mm
Priority time: 29-Nov-79 6:02:47
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>RIVESTMIT.CIF;5
File creation date: 3-Dec-79 19:40:08
Bounding box = 2250 x 2250 microns, Area = 5.06 sq mm

M.I.T. Priorities

ONLY ONE TO QUESTION IS Chang.
IF IT LINES UP OK, DO IT.

TRY TO FIX PASEMAN.

RANDY WILL RETRANSMIT ✓ DONE O.K.

MIT

Summary of designs from MIT, updated 4-Dec-79 3:51:33

Latest scoop from REB @ 4:45

AllenMIT

Designers: Don Allen, Jerry Burchfiel
Description: Variable Length Field Decoder
Est.BB: 2500 x 2500 microns

Space is allocated.
Reserved space = 2484 x 2218 microns, Area = 5.51 sq mm
Priority time: 28-Nov-79 7:45:20
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>ALLENMIT.CIF;1
File creation date: 28-Nov-79 7:45:20
Bounding box = 2484 x 2218 microns, Area = 5.51 sq mm

;2

BataliMIT

Designers: John Batali
Description: Zero-Crossing Detector for Image Processing
Est.BB 2650 x 1575

Space is allocated.
Reserved space = 2626 x 1626 microns, Area = 4.27 sq mm
Priority time: 29-Nov-79 5:59:14
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>BATALIMIT.CIF;2
File creation date: 29-Nov-79 5:59:14
Bounding box = 2626 x 1626 microns, Area = 4.27 sq mm

;3

BodonyMIT

Designers: Larry Bodony, Bruce Rose
Description: Logic State Analyzer
Est.BB: 4500 x 2650 microns

Space is allocated.
Reserved space = 924 x 944 microns, Area = 0.87 sq mm
Priority time: 28-Nov-79 7:45:43
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>BODONYMIT.CIF;1
File creation date: 28-Nov-79 7:45:43
Bounding box = 924 x 944 microns, Area = 0.87 sq mm

probable? (check recent mail)

ChangMIT

Designers: Frank Chang, Doug Williams
Description: Error-detecting block transfer oriented channel interface
Est.BB: 2500 x 5250 microns

Space is allocated.
Reserved space = 5250 x 2500 microns, Area = 13.13 sq mm
Priority time: 29-Nov-79 5:59:33
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>CHANGMIT.CIF;2
File creation date: 29-Nov-79 5:59:33
Bounding box = 5250 x 2500 microns, Area = 13.13 sq mm

;4

ChuMIT

Designers: Tam-Anh Chu, Nhi-Anh Chu, Steve McCormick
Description: Second order digital filter stage
Est.BB: 2400 x 6200 microns

Space is allocated.
Reserved space = 6146 x 2296 microns, Area = 14.11 sq mm
Priority time: 29-Nov-79 6:00:45
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>CHUMIT.CIF;2
File creation date: 29-Nov-79 6:00:45
Bounding box = 6146 x 2296 microns, Area = 14.11 sq mm

;3

FichtenbaumMIT

Designers:Matt Fichtenbaum
Description: A digital pulse rate monitor
Est.BB: 2500 x 2500 microns

Space is allocated.
Reserved space = 2500 x 2500 microns, Area = 6.25 sq mm
Priority time: 29-Nov-79 15:05:20
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>FICHTENBAUMMIT.CIF;1
File creation date: 29-Nov-79 15:05:20
Bounding box = 2500 x 2500 microns, Area = 6.25 sq mm

~~GandlerMIT~~

Designers: William Gandler
Description: Controller for 4 phase motor
Est.BB: 2000 x 2000 microns

Design is not ready for space allocation.
No file has been submitted for implementation.

GoddeauMIT

Designers:David Goddeau, Jonathan Sieber, Chris Terman
Description: A first-in, priority-out buffer
Est.BB:3000 x 3000 microns

Space is allocated.
Reserved space = 2928 x 2954 microns, Area = 8.65 sq mm
Priority time: 28-Nov-79 15:16:15
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>GODDEAUMIT.CIF;2
File creation date: 4-Dec-79 0:07:02
Bounding box = 2928 x 2954 microns, Area = 8.65 sq mm

GoodrichMIT

Designers: Earl Goodrich
Description: CRT controller
Est.BB: 2000 x 1600 microns

Space is allocated.
Reserved space = 1862 x 1546 microns, Area = 2.88 sq mm
Priority time: 1-Dec-79 20:52:01
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>GOODRICHMIT.CIF;1
File creation date: 1-Dec-79 20:52:01
Bounding box = 1862 x 1546 microns, Area = 2.88 sq mm

GramlichMIT

Designers: Wayne Gramlich, Carl Seaquist
Description: A writable PLA in which the programming of the AND and
OR planes is defined by contents of the static RAM cells.
Also cam program feedback loops to form finite state machines.
Est.BB: 2200 X 1700 microns.

Design is awaiting allocation.
Required space = 1524 x 1906 microns, Area = 2.90 sq mm
Priority time: 27-Nov-79 10:13:36
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>GRAMLICHMIT.CIF;2
File creation date: 29-Nov-79 19:35:37
Bounding box = 1524 x 1906 microns, Area = 2.90 sq mm

GrondalskiMIT

Designers: Robert Grondalski
Description: Writeable PLA
Est.BB: 2200 x 2200 microns

Space is allocated.
Reserved space = 2200 x 2200 microns, Area = 4.84 sq mm
Priority time: 28-Nov-79 7:47:36
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>GRONDALSKIMIT.CIF;1
File creation date: 28-Nov-79 7:47:36
Bounding box = 2200 x 2200 microns, Area = 4.84 sq mm

HamiltonMIT

j2

Designers:Brian Hamilton
Description: Digital Alarm Clock
Est.BB: 2500 x 2500 microns

Space is allocated.
Reserved space = 2500 x 2500 microns, Area = 6.25 sq mm
Priority time: 1-Dec-79 11:25:05
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>HAMILTONMIT.CIF;1
File creation date: 1-Dec-79 11:25:05
Bounding box = 2500 x 2500 microns, Area = 6.25 sq mm

~~JangMIT~~ delete

Designers: Saquib Jang
Description: Priority storage system
Est.BB: 2500 x 1400 microns

Design is not ready for space allocation.
Current submittal is not implementable.
File name: [Maxc]<MIT-VLSI>JANGMIT.CIF;1
File creation date: 28-Nov-79 7:47:59

KathailMIT

j3

Designers: Vinod Kathail, Keshav Pingali
Description: an interpreter for mapping programs onto a data flow computer
Est.BB: 2250 x 1750 microns

Space is allocated.
Reserved space = 904 x 1168 microns, Area = 1.06 sq mm
Priority time: 29-Nov-79 6:01:43
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>KATHAILMIT.CIF;2
File creation date: 29-Nov-79 6:01:43
Bounding box = 904 x 1168 microns, Area = 1.06 sq mm

KhouryMIT

j2

Designers: John Khoury
Description: Up-Down counter with programmable modulus
Est.BB: 2000 x 1725 microns

Space is allocated.
Reserved space = 2000 x 1726 microns, Area = 3.45 sq mm
Priority time: 29-Nov-79 15:06:27
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>KHOURYMIT.CIF;1
File creation date: 29-Nov-79 15:06:27
Bounding box = 2000 x 1726 microns, Area = 3.45 sq mm

MayleMIT delete (probably won't make it)

Designers: Neil Mayle
Description: Crossbar for AI inference networks
Est.BB: 2000 x 2000 microns

Design is awaiting allocation.
Required space = 2250 x 2126 microns, Area = 4.78 sq mm
Priority time: 30-Nov-79 12:04:36
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>MAYLEMIT.CIF;2
File creation date: 3-Dec-79 10:45:41
Bounding box = 2250 x 2126 microns, Area = 4.78 sq mm

PasemanMIT ;2

Designers: Bill Paseman
Description: Music Synthesizer
Est.BB:4250 x 1750 microns

Space is allocated.
Reserved space = 4438 x 2944 microns, Area = 13.07 sq mm
Priority time: 30-Nov-79 13:07:25
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>PASEMANMIT.CIF;1
File creation date: 30-Nov-79 13:07:25
Bounding box = 4438 x 2944 microns, Area = 13.07 sq mm

PicardMIT ;3

Designers: Len Picard
Description: Variable format field extractor and compactor
Est.BB: 2000 x 1750 microns

Space is allocated.
Reserved space = 2000 x 1688 microns, Area = 3.38 sq mm
Priority time: 29-Nov-79 6:02:08
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>PICARDMIT.CIF;2
File creation date: 29-Nov-79 6:02:08
Bounding box = 2000 x 1688 microns, Area = 3.38 sq mm

[redacted] deleted

Designers: Andrew Ressler, Carl Hewitt, Phyliss Koton
Description: communications chip for interconnecting processors
in a multiple processor system
Est.BB: 4500 x 5200 microns

Space is allocated.
Reserved space = 5000 x 5898 microns, Area = 29.49 sq mm
Priority time: 1-Dec-79 20:52:33
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>RESSLERMIT.CIF;2
File creation date: 1-Dec-79 20:52:33
Bounding box = 5000 x 5898 microns, Area = 29.49 sq mm

RiesMIT ? delete (probably untouched it)

Designers: Paul Ries
Description: Dual rail, self-timed FIFO, arbiter test circuits
Est.BB: 2000 x 2000 microns

Space is allocated.
Reserved space = 1720 x 1096 microns, Area = 1.89 sq mm
Priority time: 29-Nov-79 6:02:30
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>RIESMIT.CIF;2
File creation date: 29-Nov-79 6:02:30
Bounding box = 1720 x 1096 microns, Area = 1.89 sq mm

RivestMIT ;5

Designers: Ron Rivest, Len Adleman, Adi Shamir
Description: Section of a Multiplier
Est.BB: 2000 x 2000

```
Space is allocated.
Reserved space = 2250 x 2250 microns, Area = 5.06 sq mm
Priority time: 29-Nov-79  6:02:47
Current submittal is acceptable for implementation.
File name: [Maxc]<MIT-VLSI>RIVESTMIT.CIF;5
File creation date:  3-Dec-79 19:40:08
Bounding box = 2250 x 2250 microns, Area = 5.06 sq mm
----------------------------------------
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