- Illinois Summary

5 proj.  
38.91 mm²

Xerox
Summary of designs from Illinois, updated 4-Dec-79 20:40:30

**AdrianUI**

Designers: Frank Adrian, Nick Fiduccia, Bud Pflug
Description: Functional equivalent of AND 2001 ALU to compare MOS, TTL
Est. BB: ~2000 x 2000 microns

Space is allocated.
Reserved space = 2710 x 4388 microns, Area = 11.89 sq mm
Priority time: 27-Nov-79 15:23:08
Current submittal is acceptable for implementation.
File name: [Maxc]<IILLINOS-VLSI>ADRIANUI.CIF:3
File creation date: 4-Dec-79 13:24:09
Bounding box = 2710 x 4388 microns, Area = 11.89 sq mm

---

**ClassUI**

Designers: Class
Description: Twos complement 4 x 4 array multiplier
Est. BB: 1200 x 1200

Space is allocated.
Reserved space = 1714 x 1498 microns, Area = 2.57 sq mm
Priority time: 27-Nov-79 11:36:20
Current submittal is acceptable for implementation.
File name: [Maxc]<IILLINOS-VLSI>CLASSUI.CIF:3
File creation date: 3-Dec-79 13:54:59
Bounding box = 1714 x 1498 microns, Area = 2.57 sq mm

---

**HanesUI**

Designers: Larry Hanes, Dave Yen
Description: Twos complement array divider
Est. BB: ~2000 x 2000 microns

Space is allocated.
Reserved space = 2616 x 2636 microns, Area = 6.90 sq mm
Priority time: 27-Nov-79 15:00:02
Current submittal is acceptable for implementation.
File name: [Maxc]<IILLINOS-VLSI>HANESUI.CIF:3
File creation date: 3-Dec-79 21:33:23
Bounding box = 2616 x 2636 microns, Area = 6.90 sq mm

---

**LuhukayUI**

Designers: Joe Luhukay
Description: Pipelined multiplier, registers also used for testability
Est. BB: ~2000 x 1250 microns

Design is awaiting allocation.
Required space = 2572 x 4140 microns, Area = 10.65 sq mm
Priority time: 22-Nov-79 21:26:35
Current submittal is acceptable for implementation.
File name: [Maxc]<IILLINOS-VLSI>LUHUUKAYUI.CIF:3
File creation date: 4-Dec-79 7:21:44
Bounding box = 2572 x 4140 microns, Area = 10.65 sq mm

---

**MontoyeUI**

Designers: Bob Montoye, Al Casavant
Description: Carry lookahead adder (soln. proposed by Gajski and Kung)
Est. BB: ~2000 x 1500 microns.

Design is awaiting allocation.
Required space = 2628 x 2626 microns, Area = 6.90 sq mm
Priority time: 27-Nov-79 8:33:09
Current submittal is acceptable for implementation.
File name: [Maxc]<IILLINOS-VLSI>MONTOYEUI.CIF:2
File creation date: 4-Dec-79 13:28:21
Bounding box = 2628 x 2626 microns, Area = 6.90 sq mm