CMU
Stanford (Forrest Baskett, Mark Horowitz)
U of Rochester (Prof. Kodama)

Voxe industries people VEREX 2 OAK PARK BEHDEND MARS

BBN (Julie Steeman, Don Allen, G. Brinfield)

Boeing Aerospace Co.
Xerox PARC (Martin Newell)

Canada Labs. (Bill Plummer)

University of Central Florida, C.O. Aron, M. Kaphelikin

Boeing Aerospace Co., PO Box 3149, Seattle, WA 98101

University of Michigan, Ronald J. Leaven, CBB +CG

Conant, 310 East Meadow Circle, Palatine, IL 60067

General Electric, William Dunn

Purdue University, Dept & Comp, Larry Snyder

Stanford, Boris, Alessandro

North Carolina State U, Wes Snyder

F. R. Smith, Research Triangle Inst, North Carolina

Kenneth Perry, George Institute, & Technology School at BE, Abingdon, MD 20102

David Smith, General Dynamics, Dyna Division, PO Box 2507, San Diego, CA

James Ellis, Duke, Durham, NC, 27706

Edwin Law, NASA, Son in the 2700 Semi, Nato, OK 8707

Jon Salaworth, Courant Institute, 251 Mercer St, New York, NY 10003

Tobias, MIT, SCE & HI-Adetics

Carl Sackett, San Francisco, John Nelson, Dept EE, Systems, 904 Powell Hill

University Park, USC, LA, CA 90007

Mike Lai, Chuck Rieger, University of Maryland, College Park, MD 20742

Frank Freitas, UC, Santa Cruz, CA

Robert Ohl, Northwestern, 632 292 5628

Gary Tegerson, ESL Electronic Systems Laboratory

Jim Miller, Gen Tech, Box 6221, Stanford, CA 94305, 415-728-2828 x934

Ronald Leaven, University of Michigan, Ann Arbor, MI 48109

Wall, Cunningham, Tektronix, Inc. PO Box 500, Portland, OR 97207

Michael Hammond, Cornell, University, 905 Upham Hill, Ithaca, NY 14853

Howard Goodman, Symcos, 1531 San Antonio Rd, Palo Alto, CA 94303

Eleanor Kornfeld, Ford Aerospace Communications Corp, Ford Road, Newport Beach, CA

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Glen Hume  MITRE  MBID/GPO  PO Box 288  Bedford, MA 01730  271-3575
José Brown  Shell Development Co.  PO Box 101  Houston, TX  77001
Richard Lindemann  Cornell University  Phillips Hall  Ithaca, NY  14853
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Ann Kitter  20  Cummings Hall  Boston University  Newton, MA  02155 (Timma and)
R.P. Adams  608 Hightower  South Haven  CA  90402
Pete Hildebrand  EE Dept  Purdue Univ.  W. Lafayette, IN  47907
Harold Carter  AFITC  Air Force
July 3, 1980
2-3624-32-201

M.I.T. Laboratory for Computer Science
545 Technology Square
Room 416
Cambridge, MA 02139

Attention: Mr. Chris Terman

Subject: VLSI Checking and Simulation Software

Dear Chris:

I enjoyed meeting you at the Design Automation Conference and would have liked to talk more if the environment and schedule had been more conducive to discussion.

As I mentioned, we are using Carver Mead's LAP software for VLSI design and have a critical need for geometric design rule checking, connectivity checking and delay simulation. I was delighted to hear that you had added connectivity checking and delay simulation to Clark Baker's design rule checker, and offered to send us a copy free of charge.

Attached to this letter is a blank magnetic tape. Please load it with the current versions of your software and Clark Baker's software, including any documentation files that you may have. When you return it, please include a copy of Clark's thesis. Also, if Ron Rivest has any literature describing his public key encryption algorithm and progress on implementing it in VLSI, I would very much appreciate you including that also. Or if you prefer, I will contact Ron directly.

You mentioned, Chris, that this software runs under UNIX version 7. Our UNIX VAX system here is running Berkeley's paging UNIX, which is an extension of version 7. If you can think offhand of any incompatibilities, I would appreciate appropriate warnings.
To: Mr. Chris Terman

We very much appreciate your willingness to share this software. It promises to solve some of the severe inadequacies of Carver Mead's system. We understand that you are under no obligation to support this software in any way and we will refrain from annoying you with questions and problems.

Yours truly,

BOEING AEROSPACE COMPANY

L. V. Corbin
Electronics Computer Aided Design
(206) 773-3369 - M/S 8H-53

Attachment (1) Tape
August 11, 1980

Boeing Aerospace Company
P.O. Box 3999
Seattle, WA 98124

Attention: L. V. Corbin

Subject: VLSI Checking and Simulation Software

Dear Mr. Corbin:

I placed the standard set of software that we have been giving out on the tape. It includes a design rule checker, circuit extractor, and two simulators, one unit-delay, and the other a more detailed timing model. Our software runs on a PDP-11/70 under their UNIX operating system. In addition, the simulators have been run on a VAX-11/780 under the VMS operating system.

Currently we do not run UNIX on our VAX. It will take some work on your part to make all of the programs run on the Berkeley UNIX. When I wrote my programs, I did not plan on them running on a 32-bit machine. So far, people have spent about two weeks changing them to run on a machine with a larger word length. There are three things to watch out for: 1) built in 16-bit constants (e.g. 32767, and -32767), 2) fixed length records in binary files where the assumption is that integers are 2 bytes (i.e. look at all fwrite, fread, and fseek), and 3) any place where I assume that the top 16 bits of a word are 0 (e.g. maybe in the design rule checker).

What all of this means is that you have three alternatives: 1) try to make the programs work yourself, 2) wait until we get Berkeley UNIX and make the programs work ourselves, or 3) get a copy of the programs from someone else (Forest Bookett at Stanford) who has already done the conversion.

Maybe you should play with the current programs, and get back to us in September when you have some experience with the programs, and we have done some more development.

The format of the magtape is simple. It is 9 track, 800 BPI, 512 byte records, one file on the tape, no header files. The file contains a little documentation along with all the programs. We hope to have more documentation in a while.

As long as it does not get out of hand, you can call me with questions at (617) 253-6043. I am usually in work by noon and leave around midnight.

Yours truly,

Clark M. Baker