

Appendix D
SITES VISITED

Bell Telephone Laboratories, Murray Hill, New Jersey (12/10/75)

W. O. Baker	R. F. W. Pease
E. I. Gordon (host)	L. Tompson
D. R. Herriott	P. A. Turner
D. Maydan	

Hughes Research Laboratories, Malibu, California (11/26/75--Mead & Sutherland)

A. Chester	G. F. Smith (host)
H. L. Garvin	P. A. Sullivan
R. Henderson	M. Waldner
F. Ozdemir	E. D. Wolf
R. L. Seliger	

IBM Corporation, Yorktown Heights, New York (1/22/76)

W. A. Bohan	M. B. Heritage
A. N. Broers	J. W. Newitt (host)
T. H. P. Chang	V. Sadagopan
D. L. Critchlow	S. Triebwasser
C. D. Cullum	J. Wilczynski
R. E. Gomory	H. Yu
M. Hatzakis	

Intel Corporation, Santa Clara, California (2/12/76--Everhart & Mead)

Gordon Moore

Lincoln Laboratory, MIT, Lexington, Massachusetts (1/21/76)

H. I. Smith (host)
J. I. Raffel

RCA Laboratories, Princeton, New Jersey (12/11/75)

J. Herzog (host)
J. Scott
W. Webster

Texas Instruments Incorporated, Dallas, Texas (12/15/75)

G. Barnell	N. Einsproch
F. Bucy	J. Pankratz (host)
T. Blocker	R. Stratton

University of Florida, Gainesville, Florida (12/05/75)--Sutherland)

D. P. Kennedy (host)
A. D. Sutherland

Eiichi Goto, Faculty of Science, University of Tokyo, Tokyo, Japan

Visited Rand on 10/17/75 and gave a talk entitled: "A Double Deflection Cathode Ray Tube"

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- Hoeneisen, B., and C. A. Mead, "Fundamental Limitations in Micro-electronics--I. MOS Technology," *Solid-State Electronics*, Vol. 15, 1972, pp. 819-829.
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- Keyes, Robert W., "Physical Limits in Digital Electronics," *Proceedings of the IEEE*, Vol. 63, No. 5, May 1975, pp. 740-767.
- Moore, Gordon E., "Progress in Digital Integrated Electronics." A paper delivered at the International Electron Devices Meeting in Washington, D.C., December 1975. To appear in the *Proceedings*. (Reprinted here as Appendix B.)
- Schwartz, M., *Information Transmission, Modulation, and Noise*, McGraw-Hill Book Company, Inc., New York, 1959, pp. 382-384.
- Sutherland, Ivan E., and Donald Oestreicher, "How Big Should a Printed Circuit Board Be?" *IEEE Transactions on Computers*, Vol. C-22, No. 5, May 1973, pp. 537-542.

