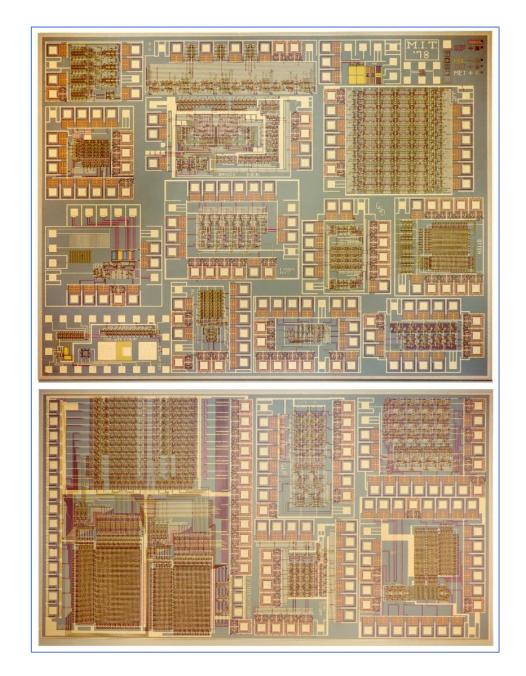
Lynn Conway's <u>IEEE-SOCC 2022</u> Dinner-Talk Visuals

FIRST FLIGHT:

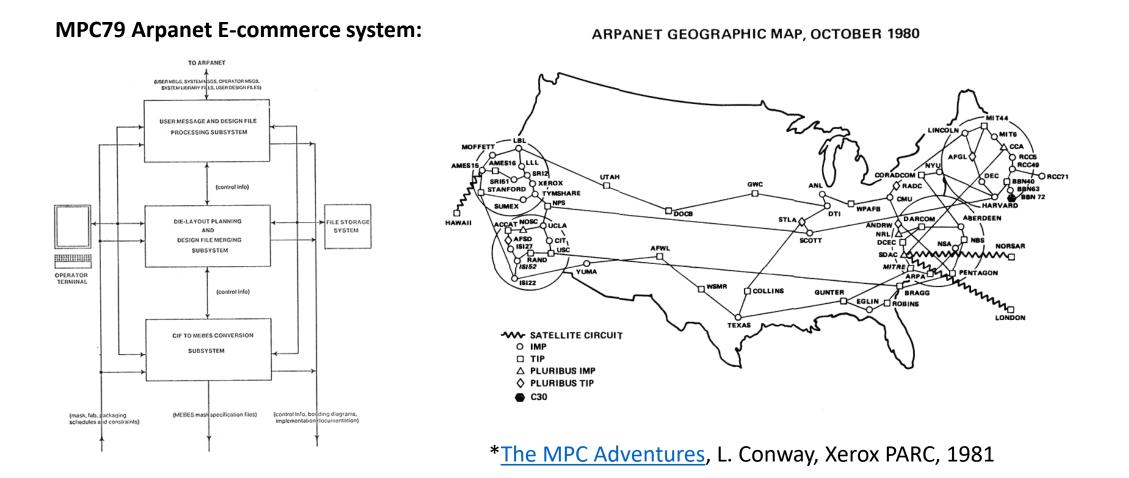
Map and photomicrograph of the 19 student projects on the MIT'78 'MultiProject' Chip

and a second	******			***	in an air i			
19. Runchan Yang	1	8. Richard S	tern 4			e Col	n MIT Test	Aligr
5. Steve Frank		Andy Bou J. Dean B Randy Br Clement L	rock yant		3.	Jim	Cherry	
1. Sandra Azoury N. Lynn Bowen Jorge Rubenstein 7. Nelson Goldikene Scott Westbrook		Ernesto Pe			aig Olson		12. Dave Otten	
		Tak T Hiratsuka	9. Siu Ho Lam			10. Dave Levitt		
17. Guy Steele			14. Gerald Roylance			15. Dave Shaver		
			16. Alan Snyder			6. Jim Frankel		

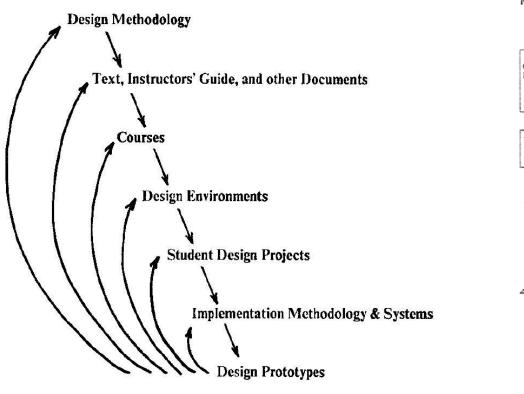
For more about the <u>MIT'78 course</u>, see <u>Lynn's "MIT Reminiscences"</u>



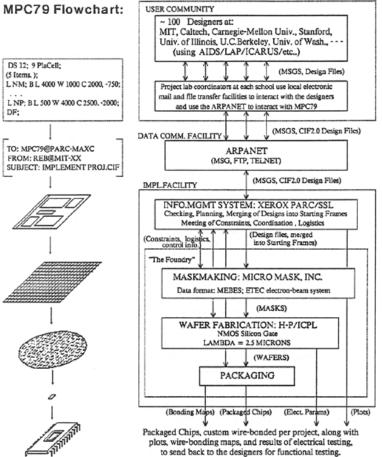
In the fall of '79, we orchestrated a huge "ARPANET Happening" (MPC79*) Involving 129 budding VLSI designers taking M-C courses at 12 research universities



<u>MPC79</u> was a large-scale demonstration and validation of the new VLSI design methods, textbook, courses, design tools and e-commerce infrastructure . . . and triggered the exponentiation of the budding VLSI system-design ecosystem.







<u>1976</u>



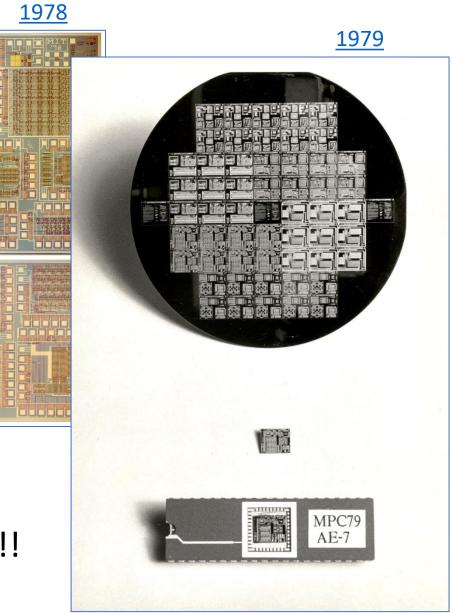
Visualizing the Launch of the VLSI Design Methods

'76: How to cope with VLSI <u>complexity</u>?

'77: Inventing scalable VLSI <u>design rules</u>.

'78: 1st Flight of the VLSI <u>methods at MIT</u>!

'79: Launching Multiple Flights via MPC79!!



Lynn Conway and her Alto in her office at Xerox PARC (1983)

Photo by Margaret Moulton



